

REPLACEMENT SHEET
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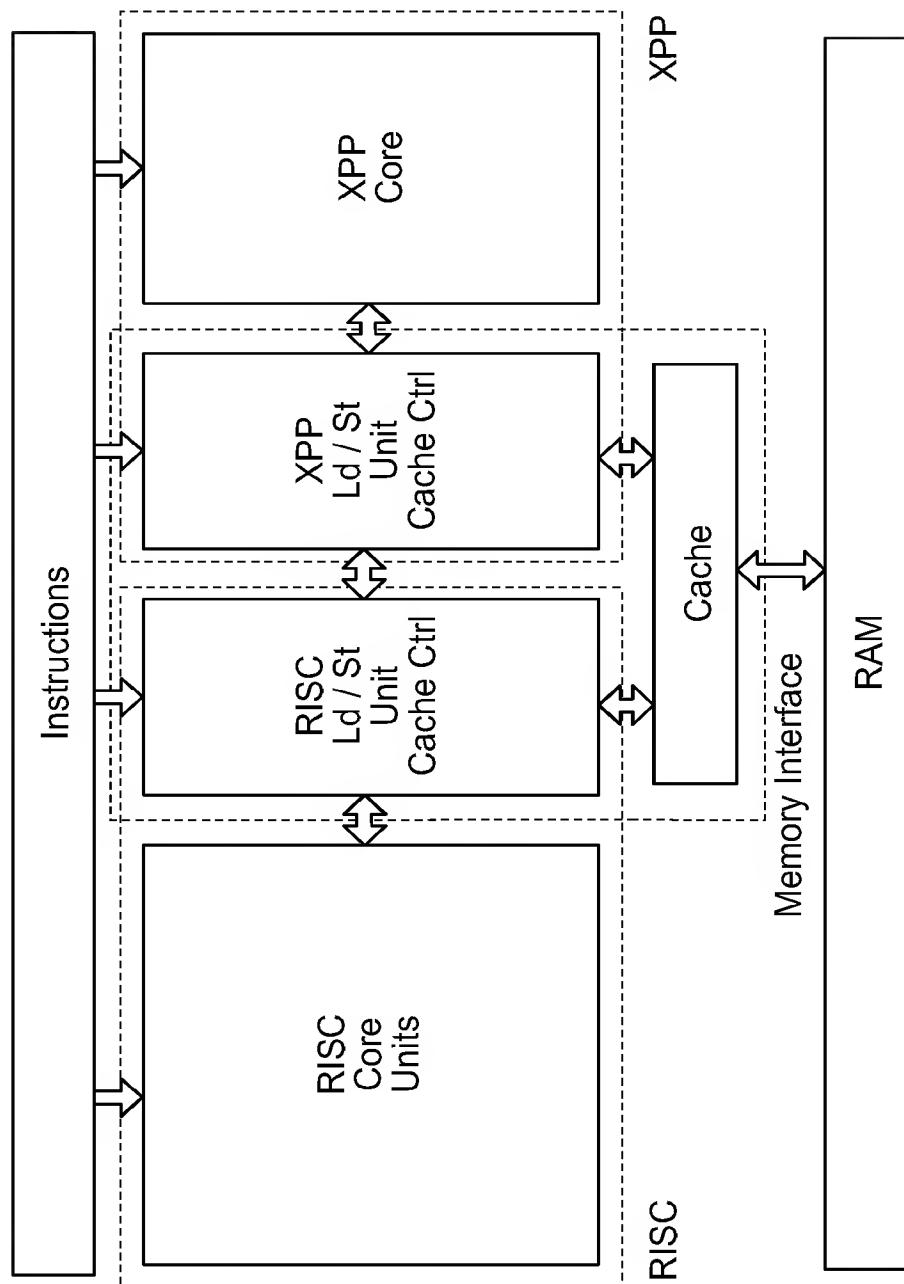
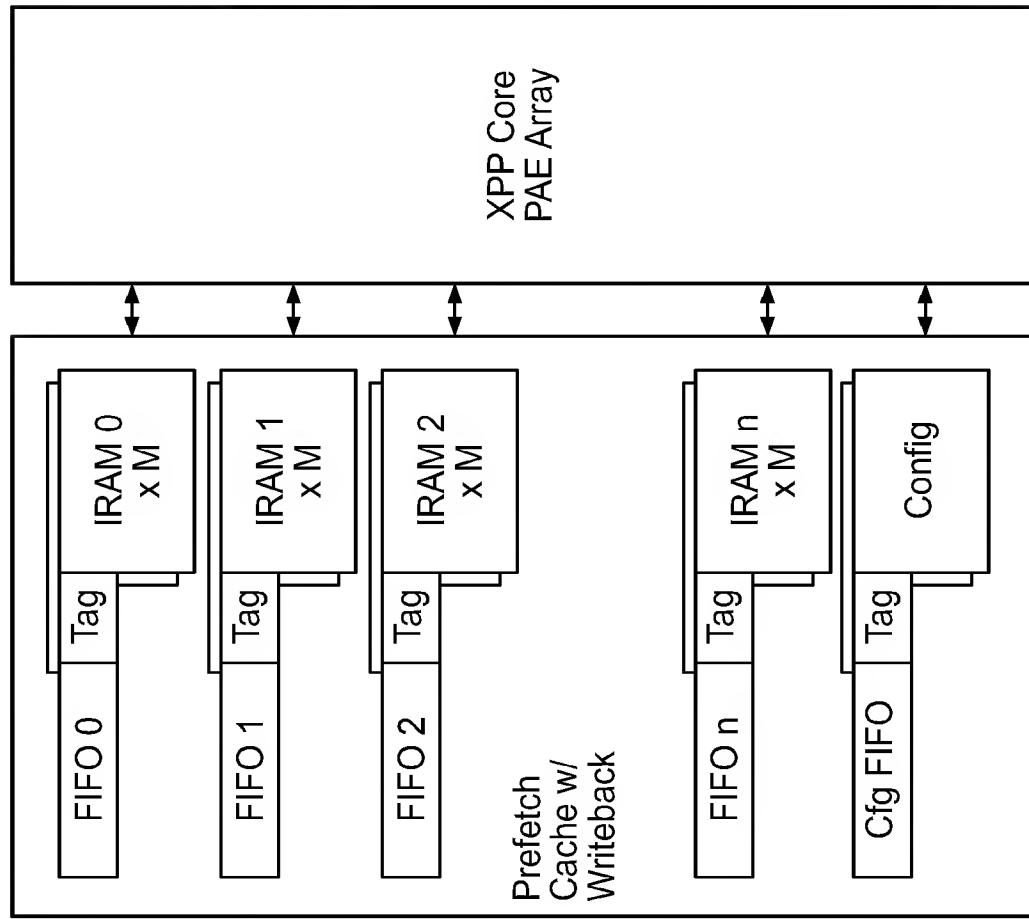


FIG. 1

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```

XPPPReloadConfig(_XppCfg_foo );
for(int i=0; i<1000; ++i){
    XPPPReload( 2, &ai[*30], 30 );
    XPPPReload( 0, &bi[*200], 200 );
    XPPPReloadClean( 5, &ci[*10], 10 );
    XPPExecute( );
}

/* Other RISC computations ...
   In the meanwhile the burst preloads and
   the previous configuration are running;
   The new configuration is executed as soon
   as the preloads and the previous
   configuration are finished.
   New burst preloads can be issued
   according to the FIFO length.
*/

```

Note: In all Places where Constants are Used,
 the Value should Actually come from a
 Register

Legend:

Per Thread State Resource
Volatile (non-state) Resource Write Back if Dirty
Volatile Read only Resource

FIG. 2

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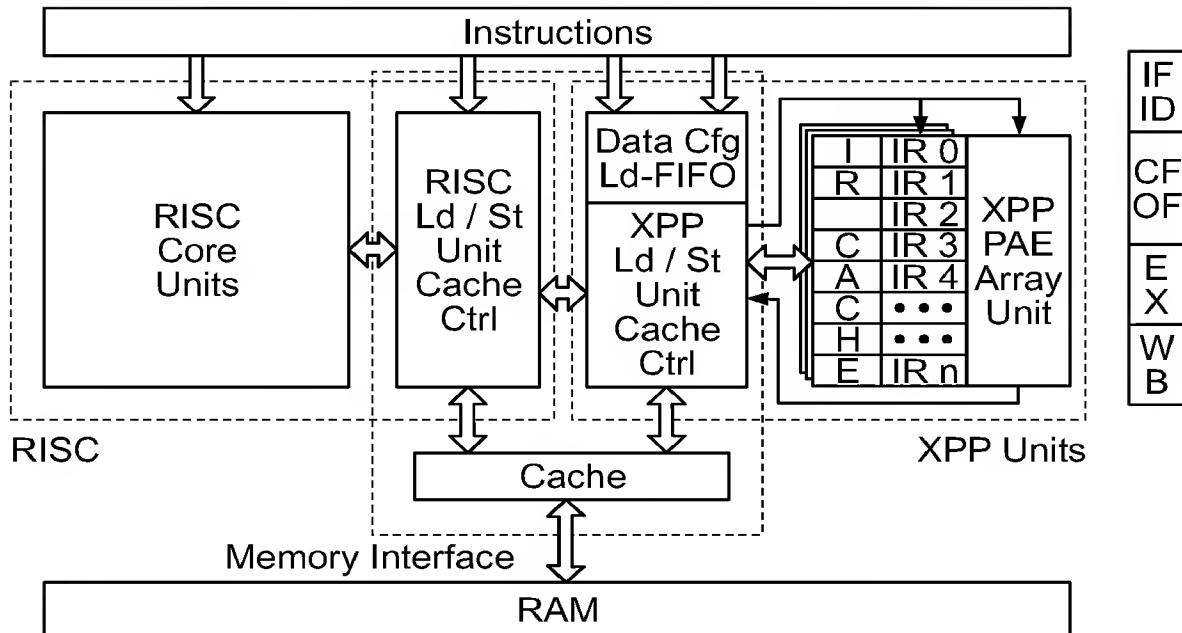


FIG. 3

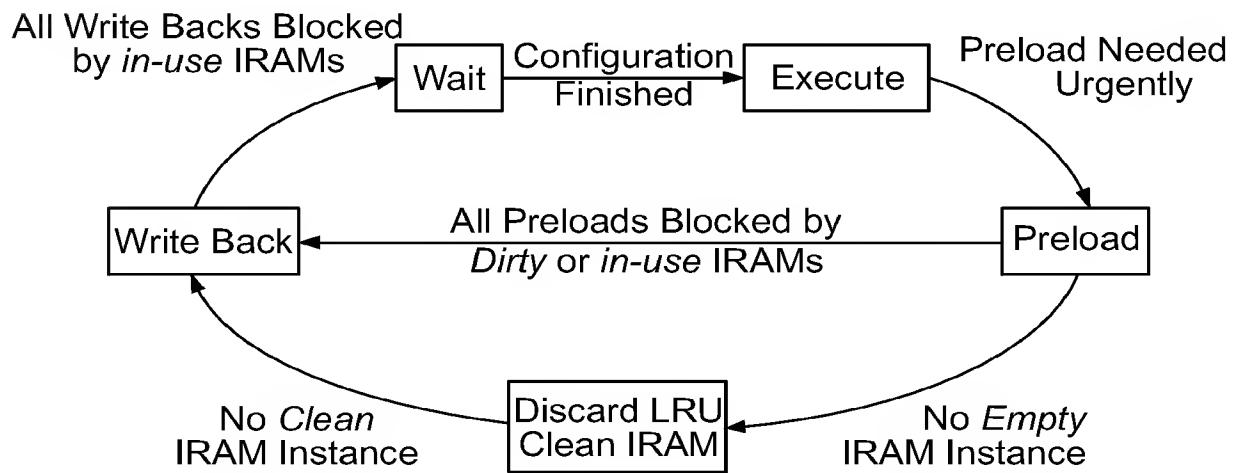


FIG. 4

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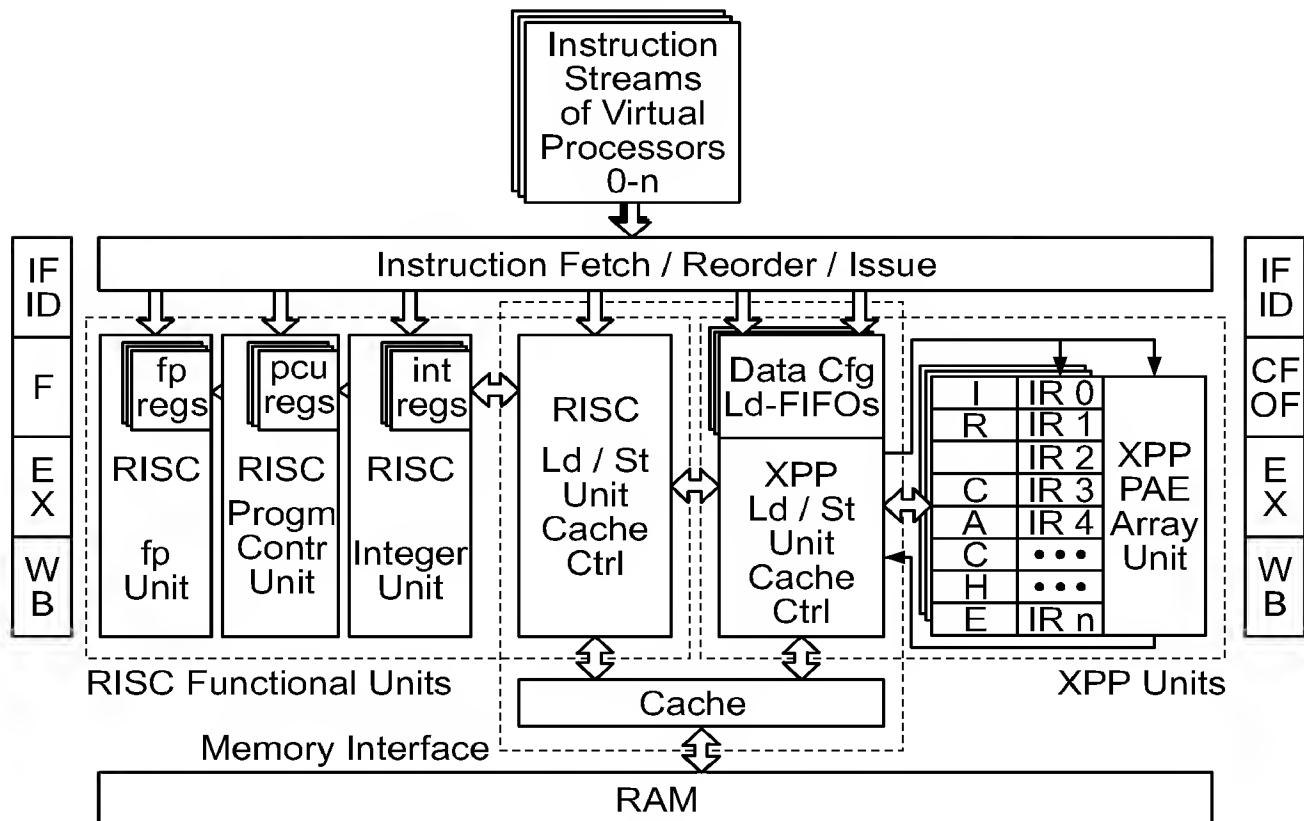


FIG. 5

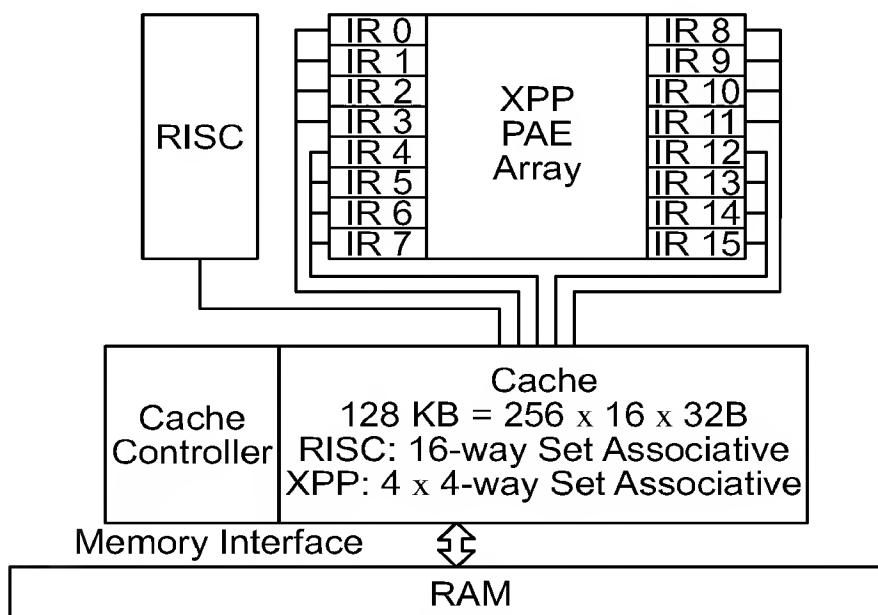


FIG. 6

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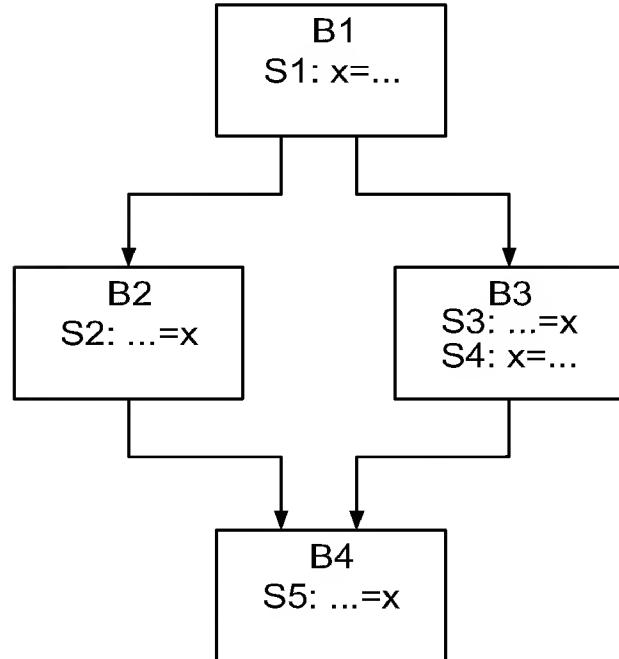


FIG. 7

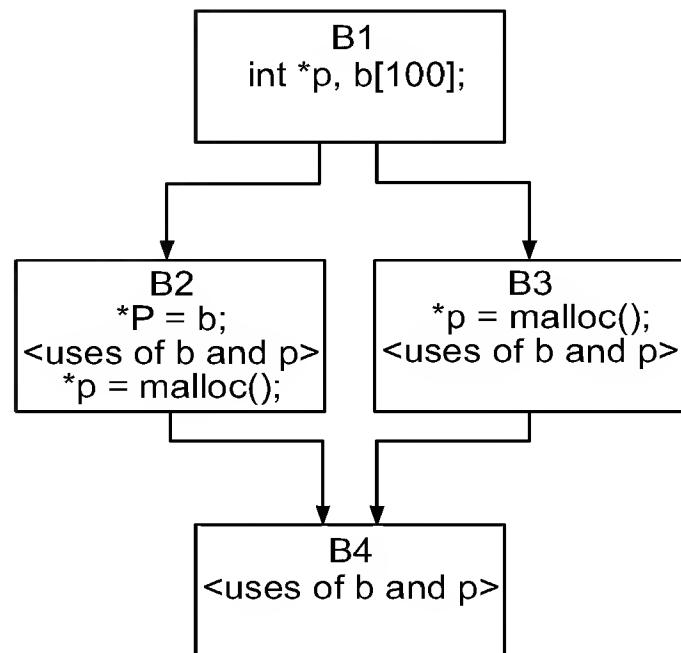


FIG. 8

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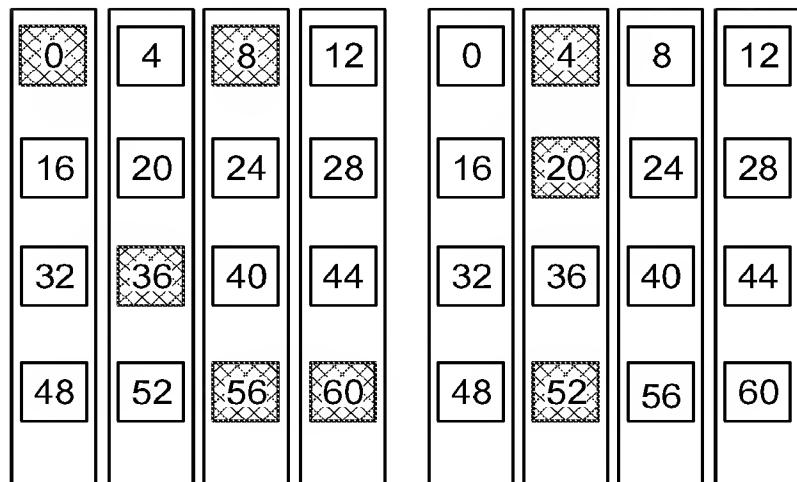


FIG. 9

```
for (j=1; j<=N-1; i++)  
    for (j=1; j<=N; j++)  
        b[i] [j] = 0.25 * (a[i-1] [j] + a[i] [j-1] +  
        a[i+1] [j] + a[i] [j+1]);
```



FIG. 10

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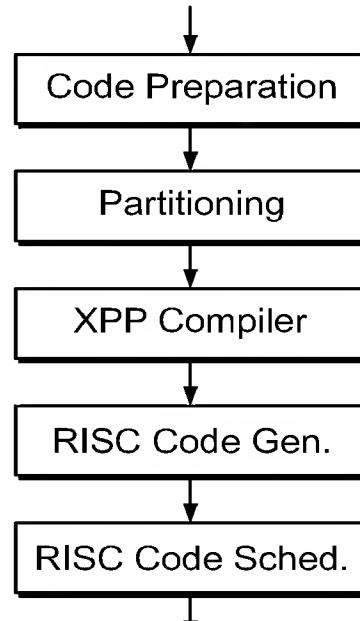


FIG. 11

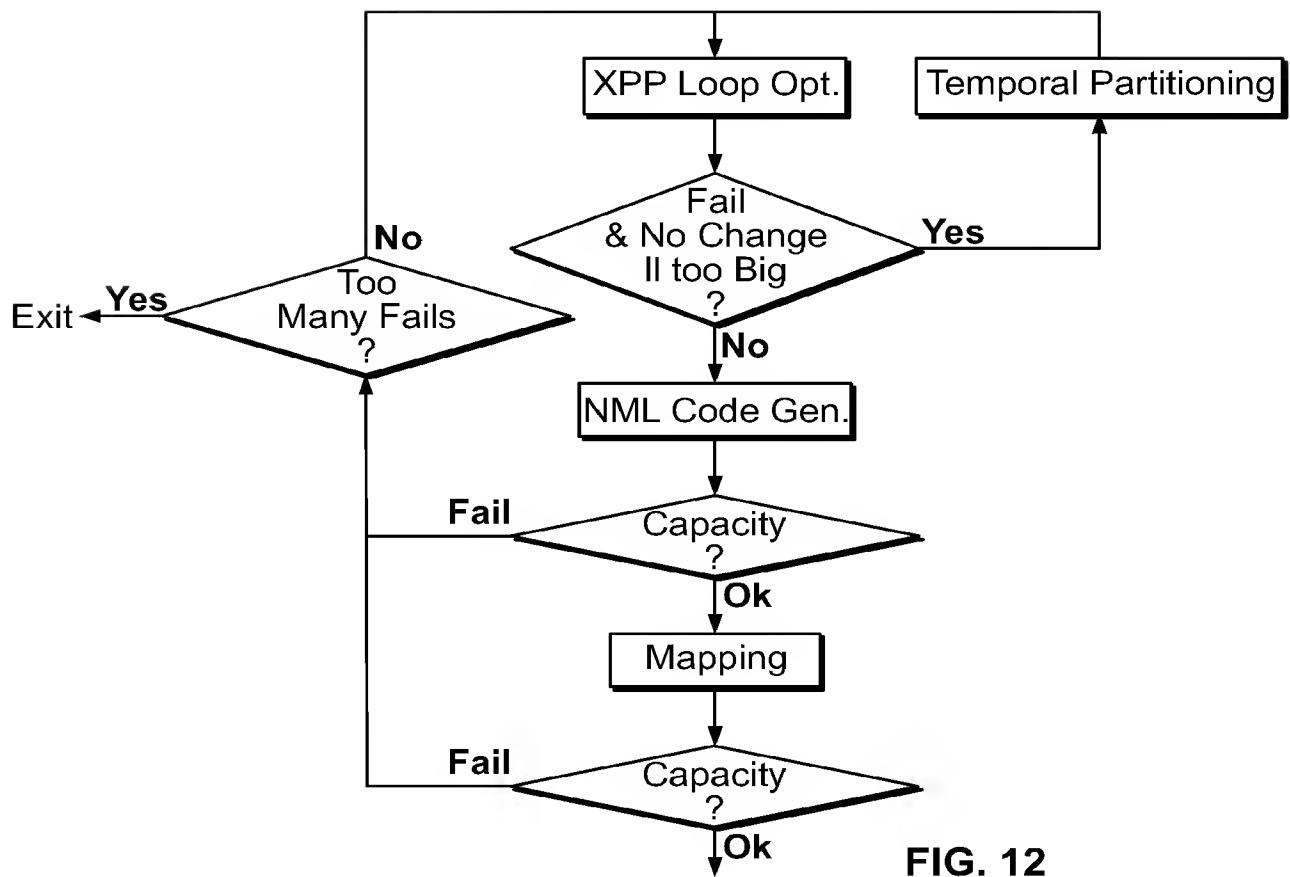


FIG. 12

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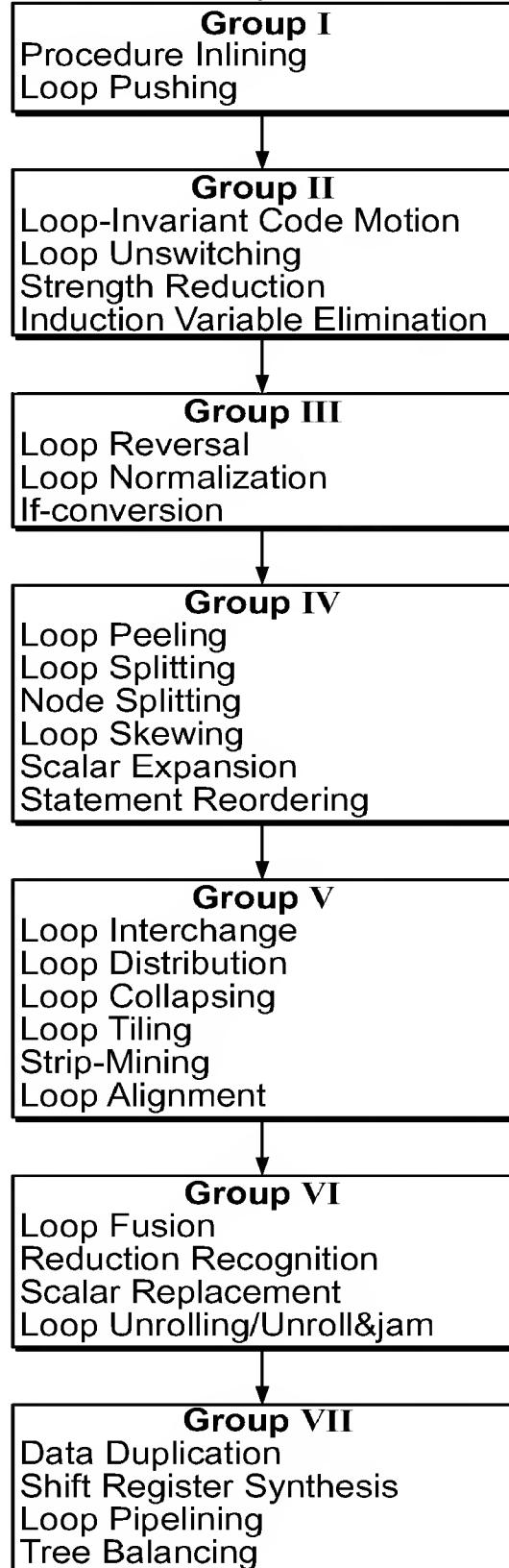


FIG. 13

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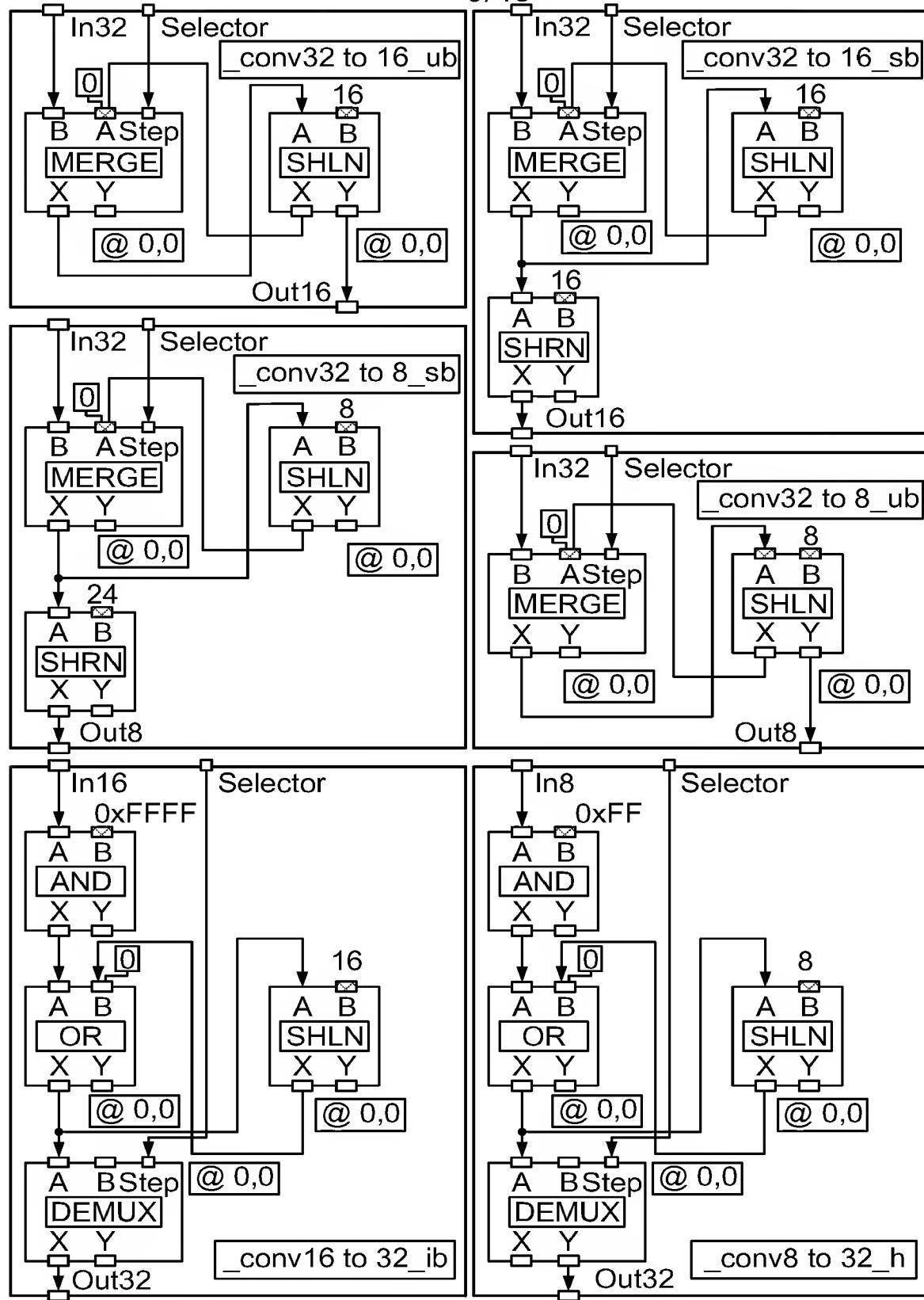


FIG. 14

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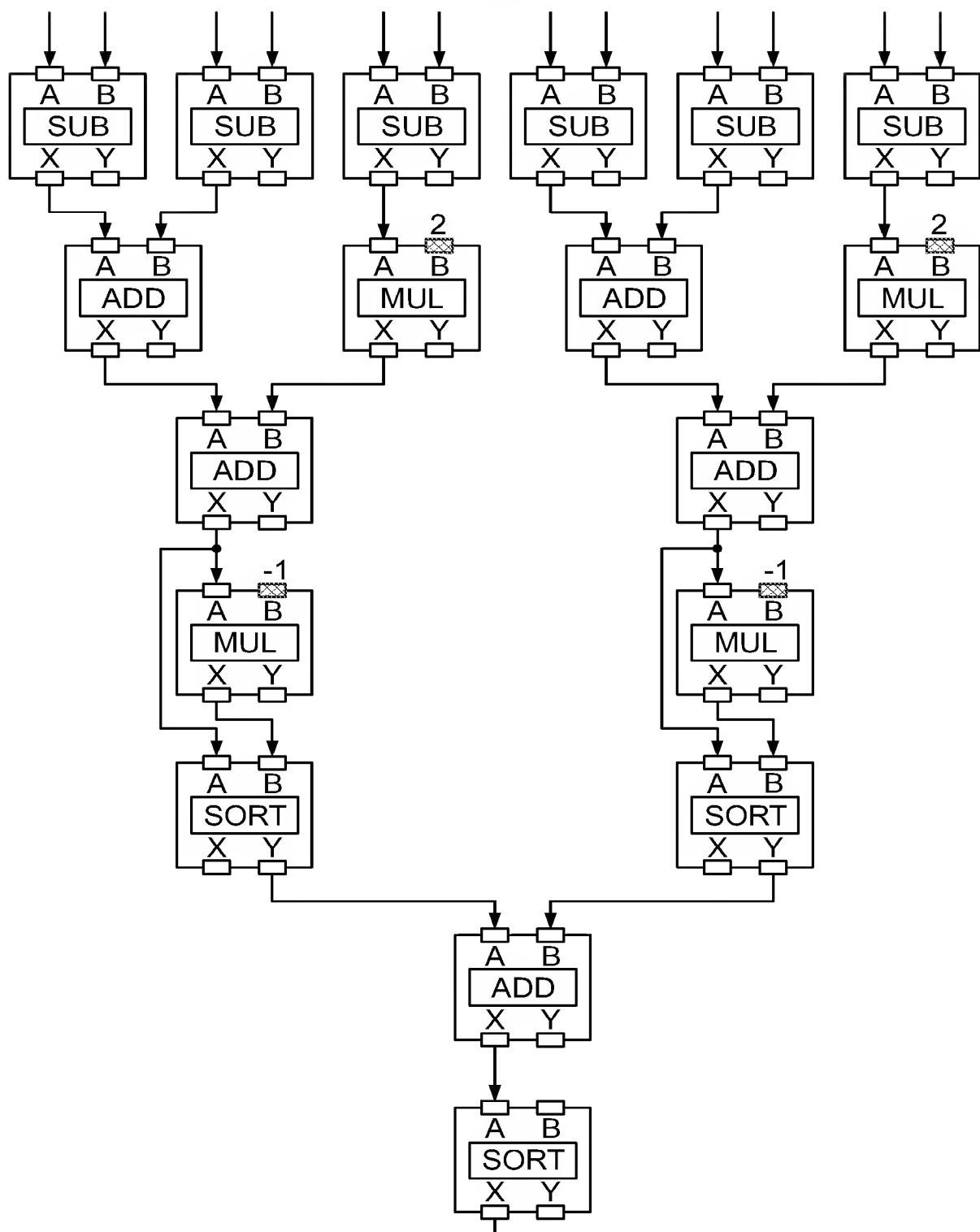


FIG. 15

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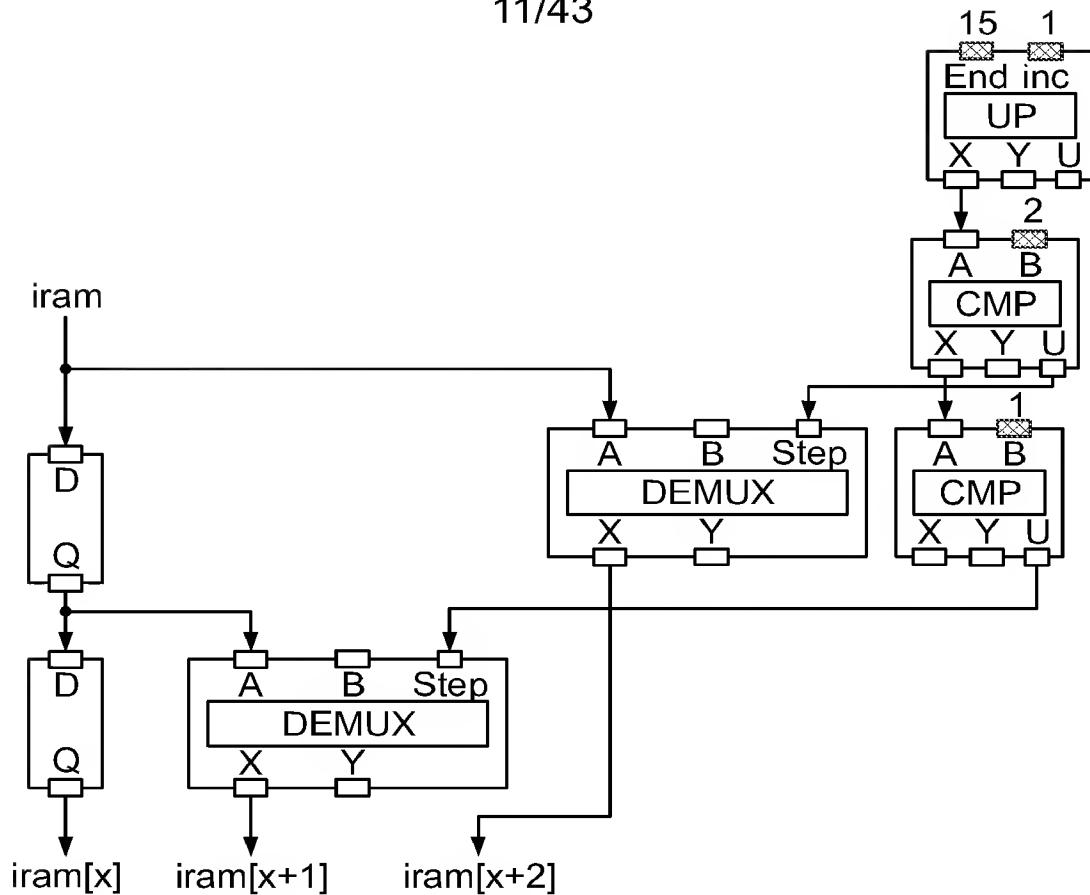


FIG. 16

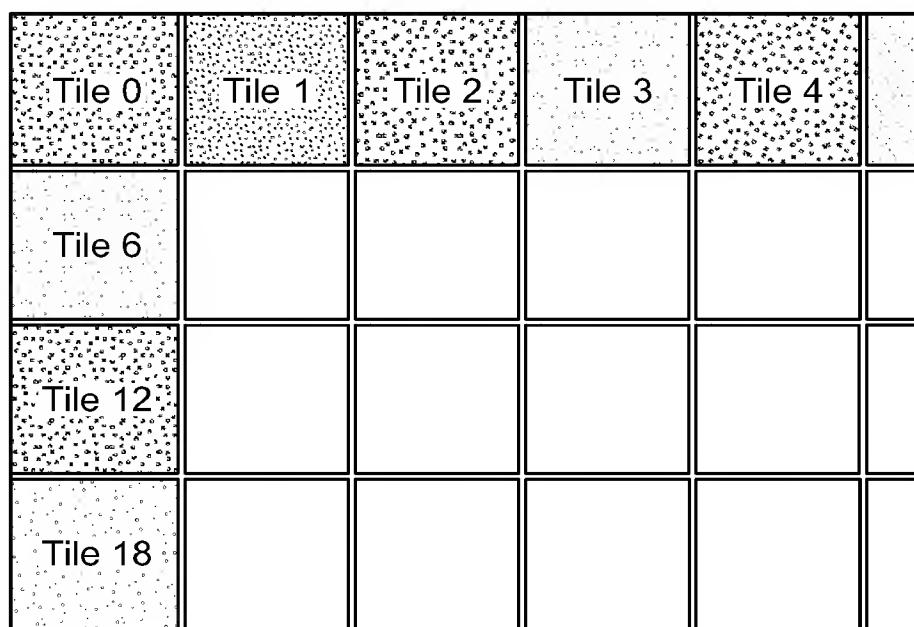
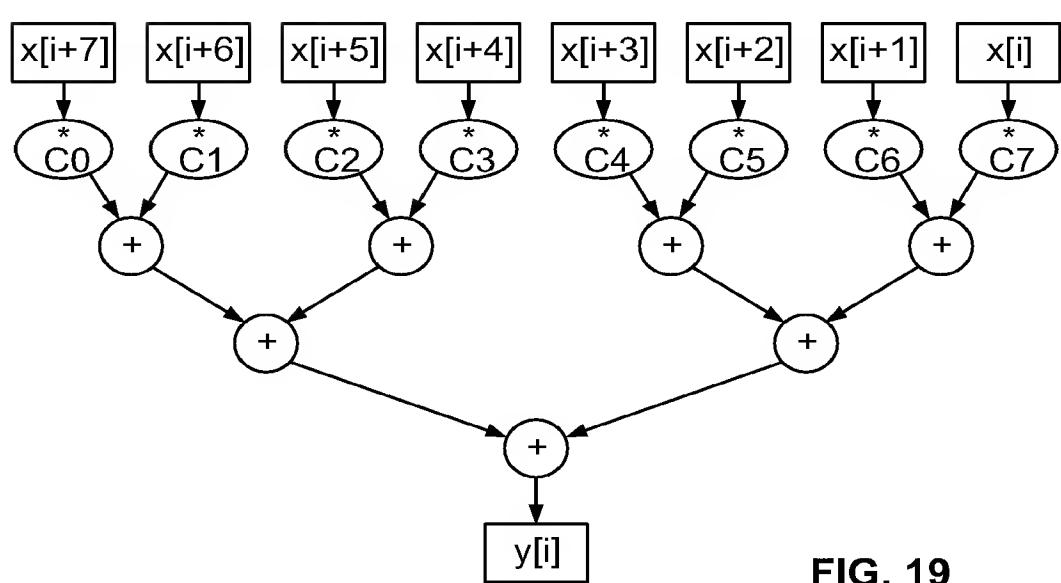
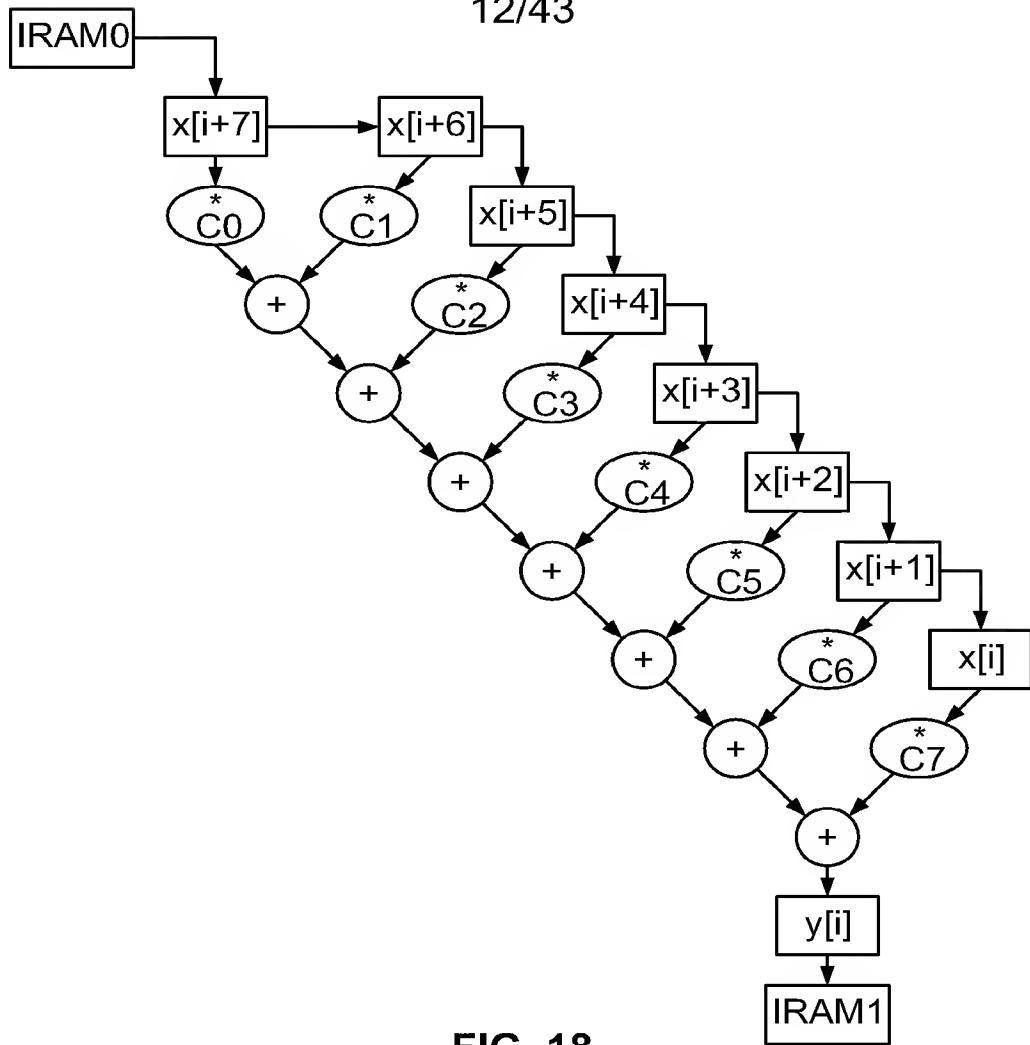


FIG. 17

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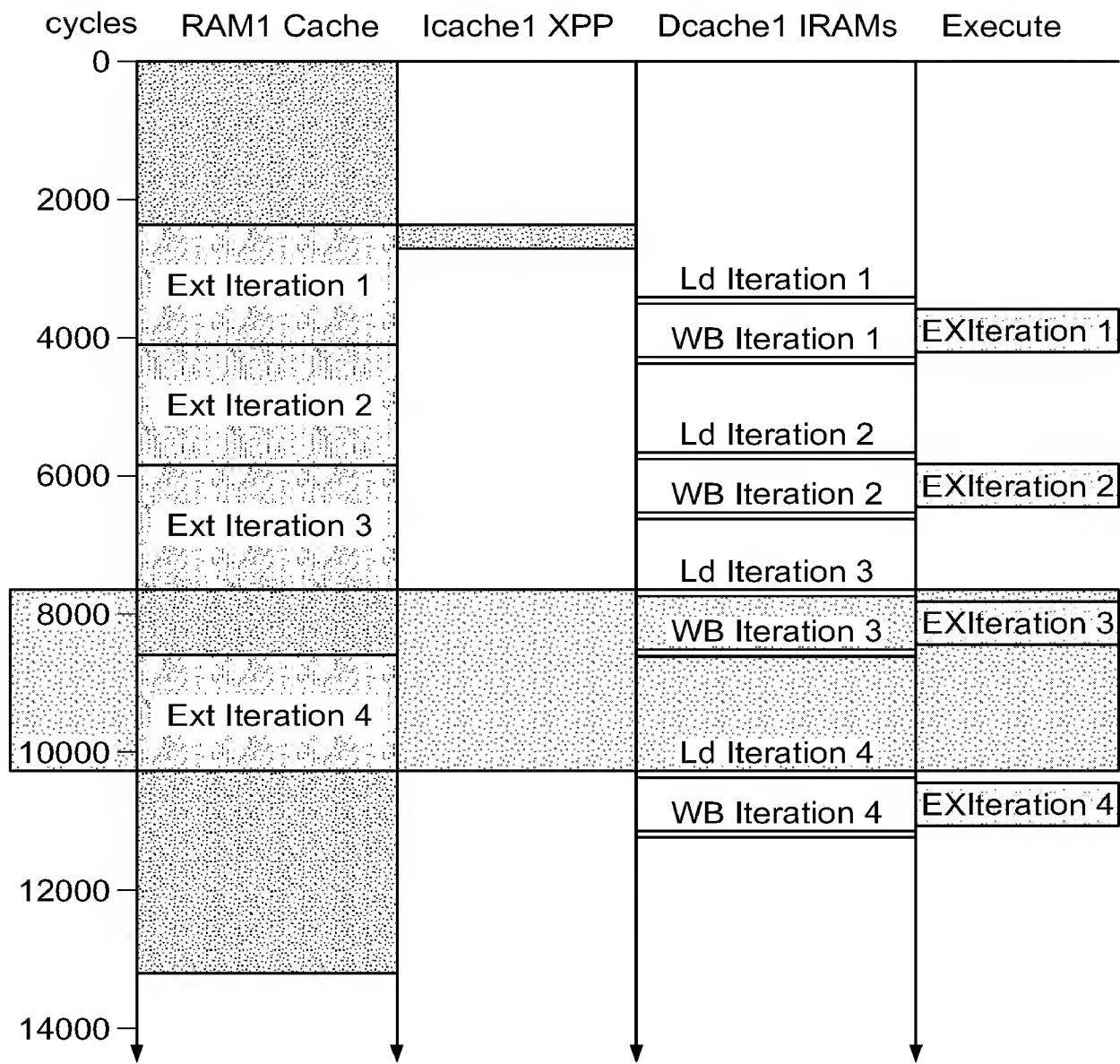


FIG. 20

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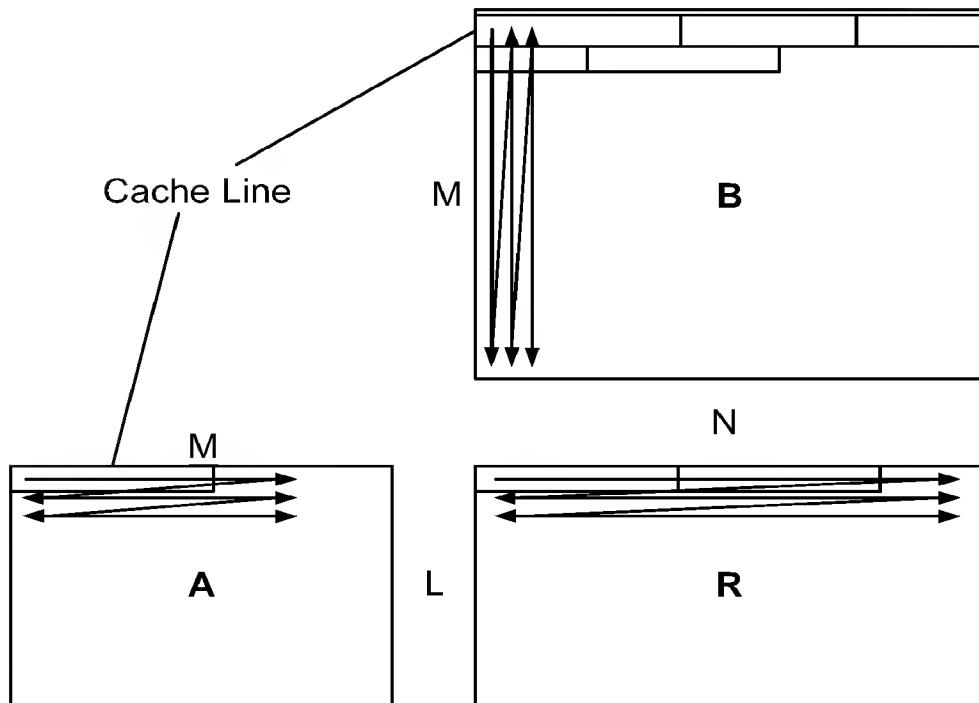


FIG. 21

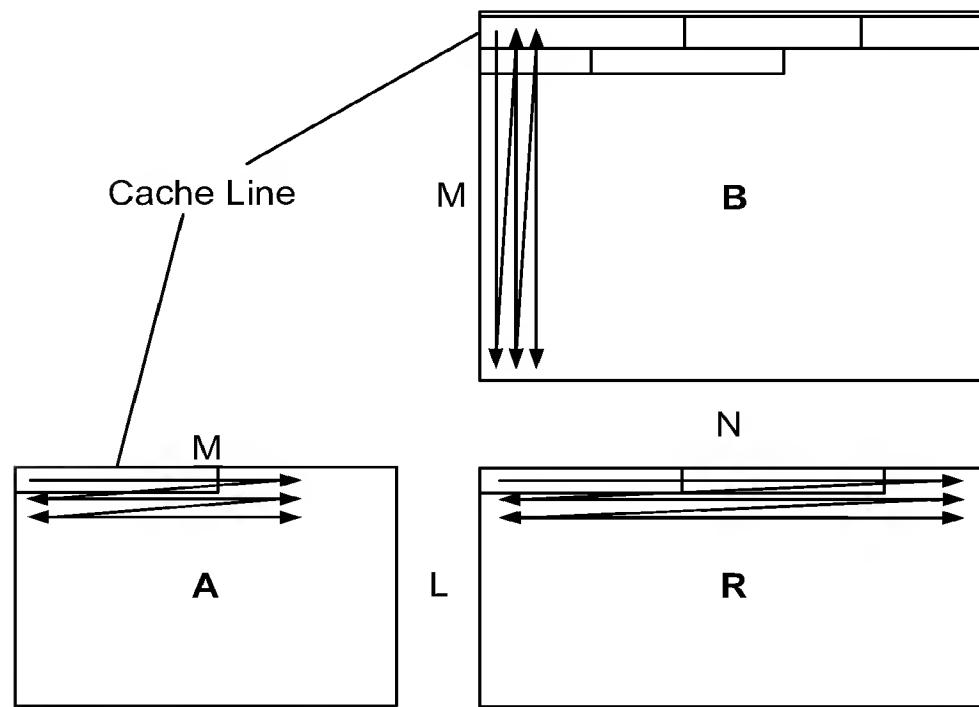


FIG. 22

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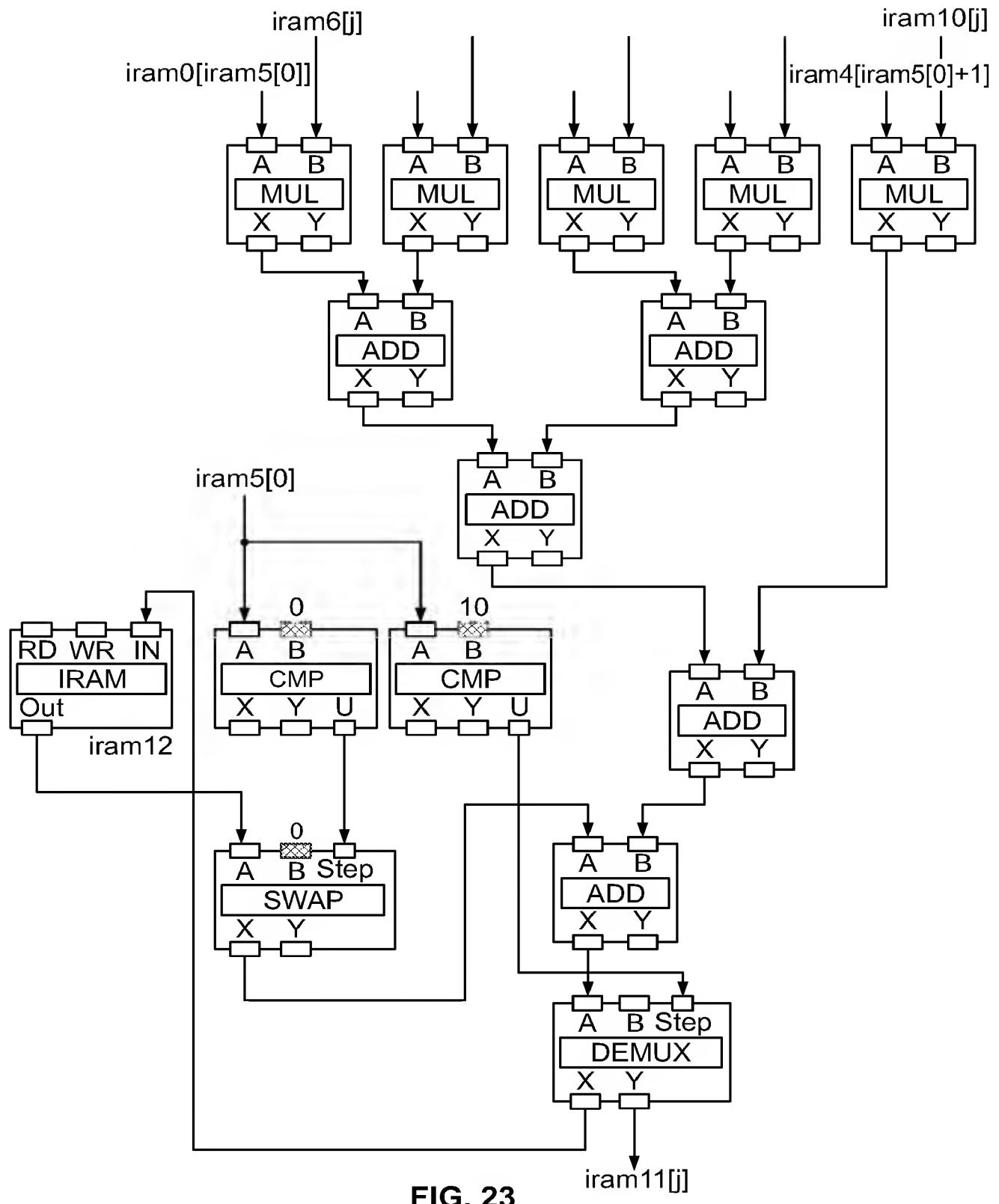


FIG. 23

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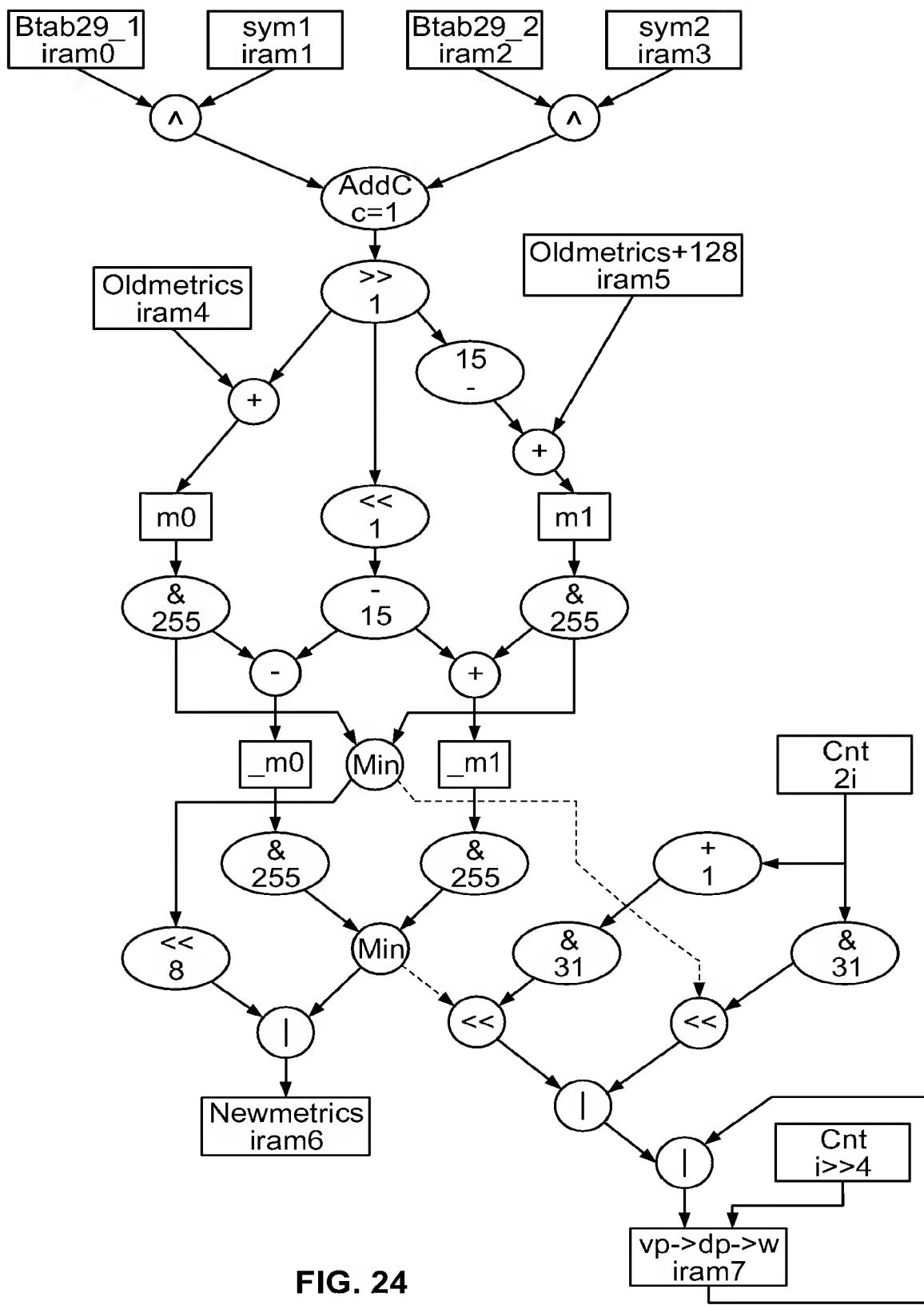


FIG. 24

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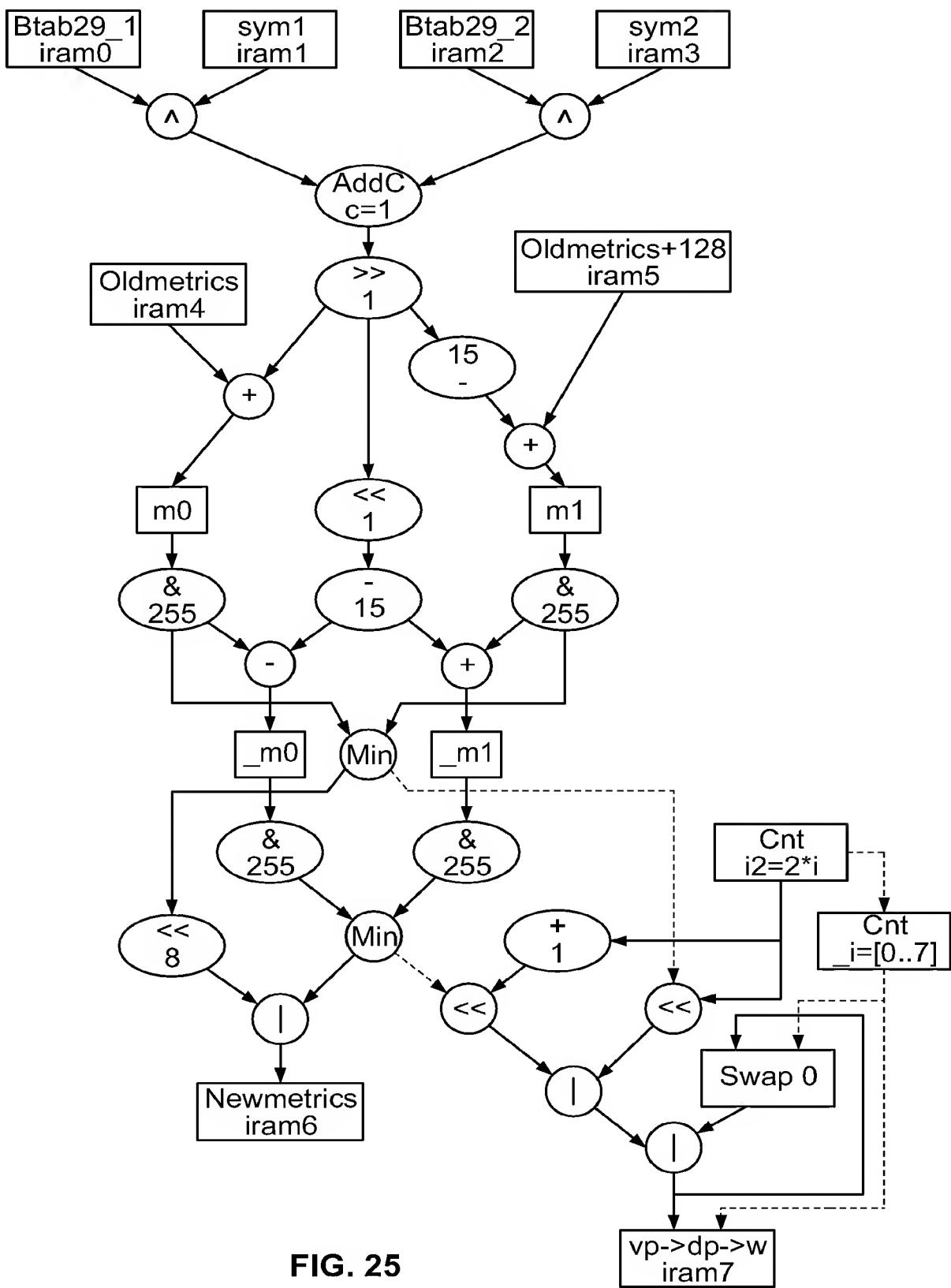


FIG. 25

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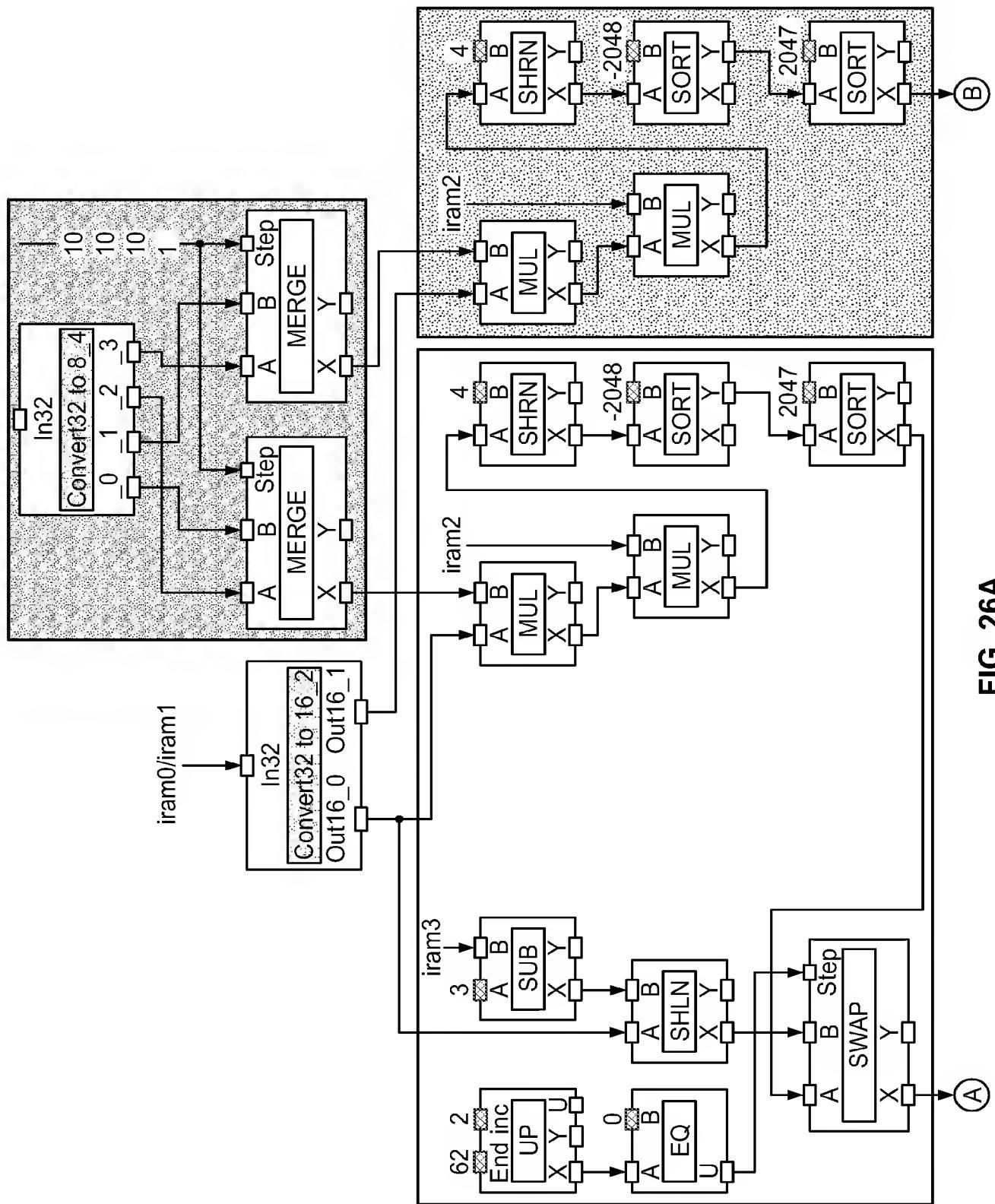


FIG. 26A

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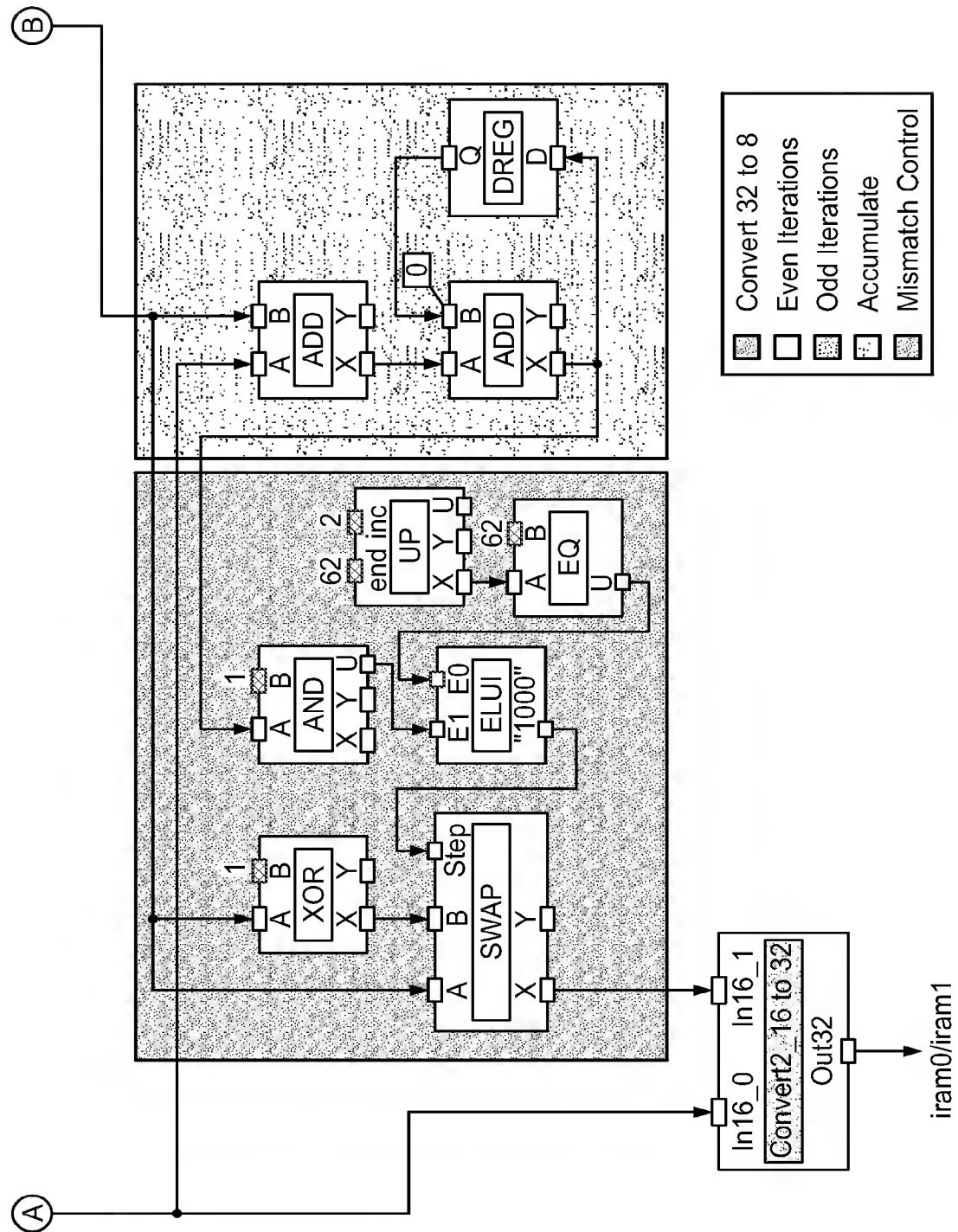


FIG. 26B

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x idctrow

$8 \times$ idctcol

result

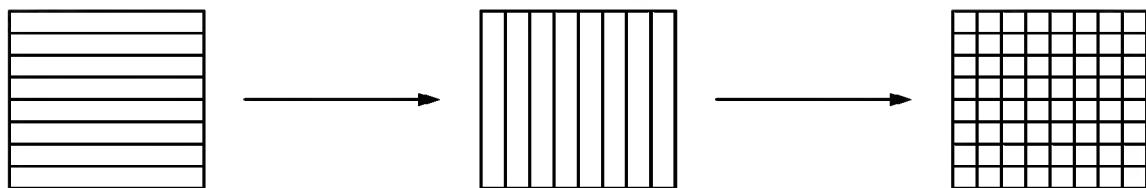


FIG. 27

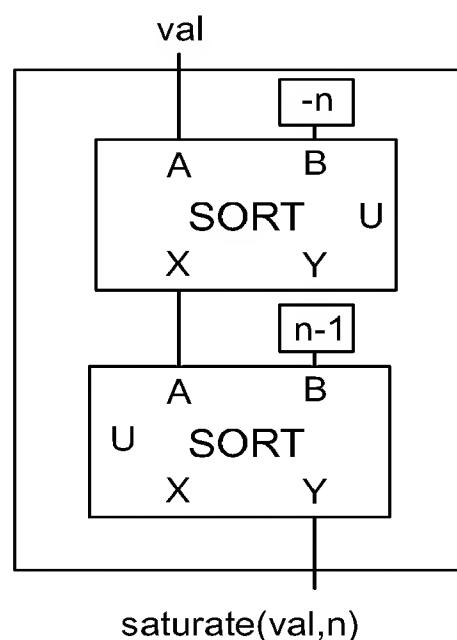


FIG. 28

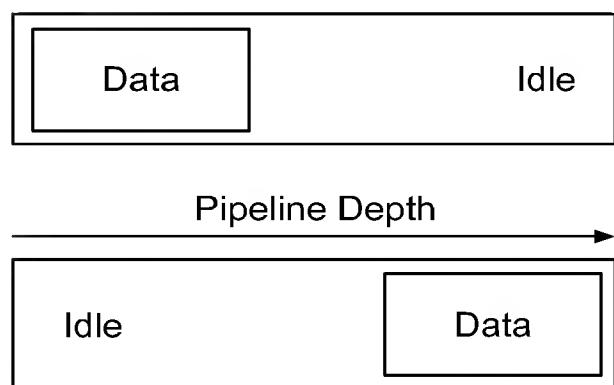
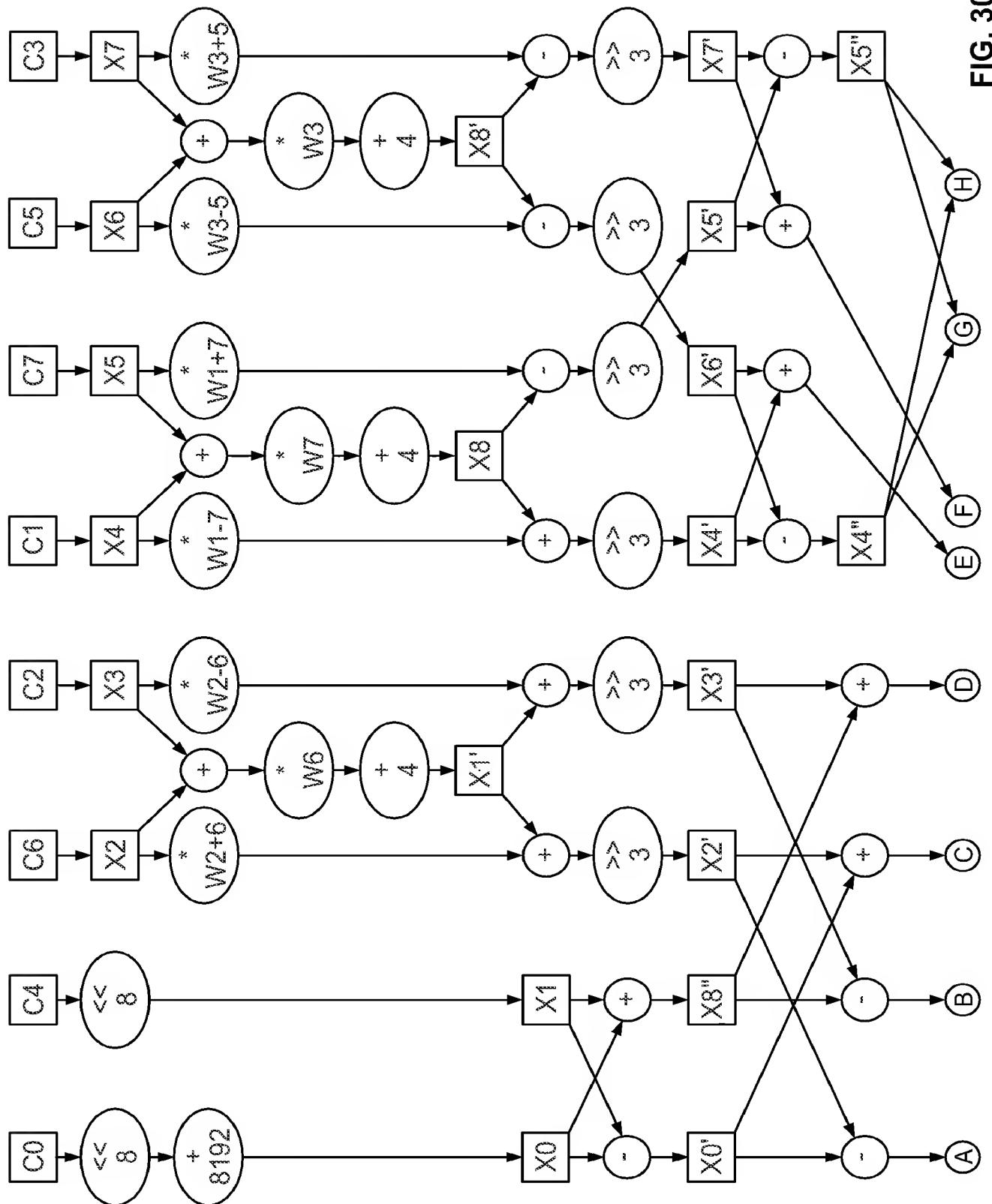


FIG. 29

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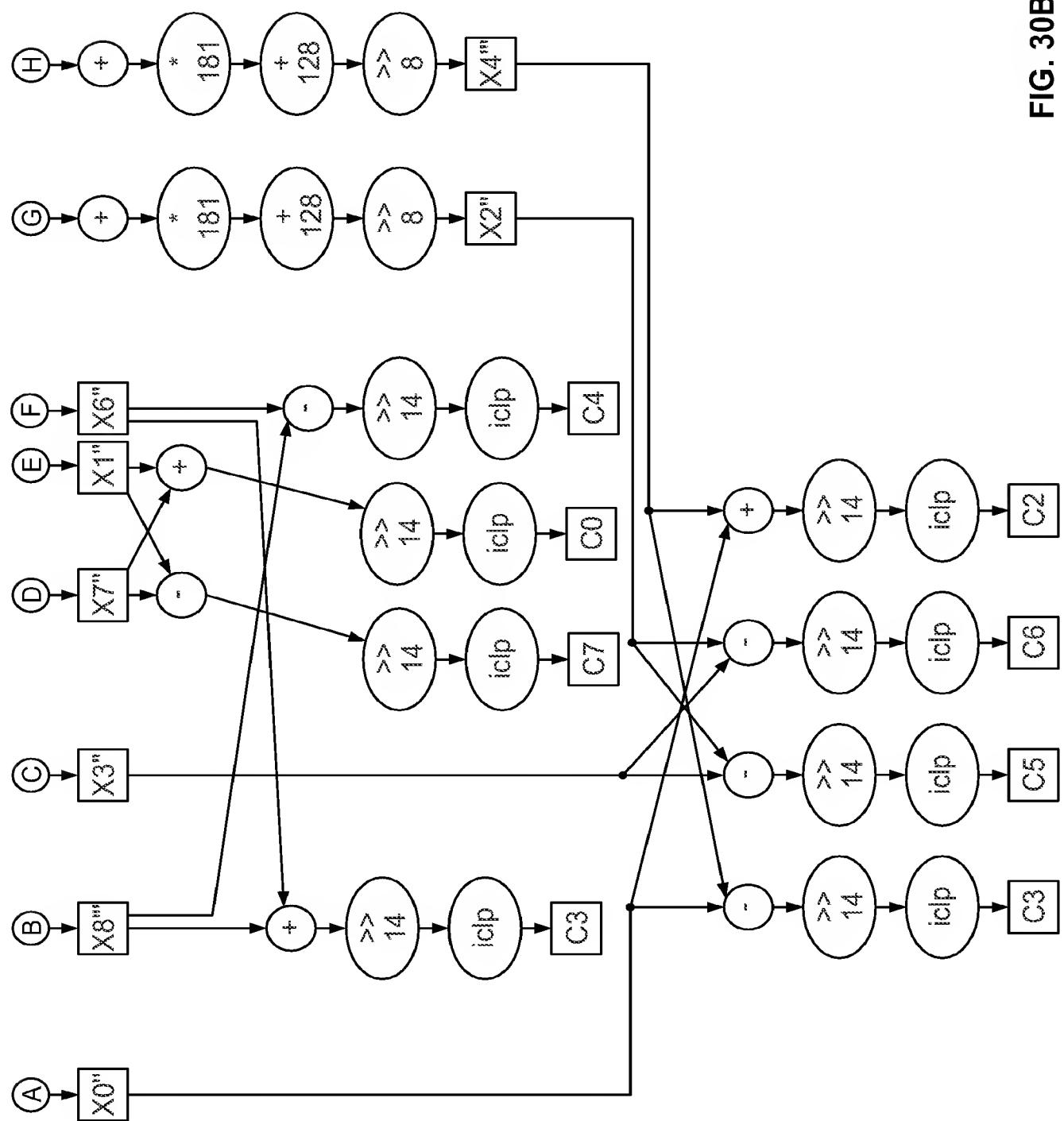


FIG. 30B

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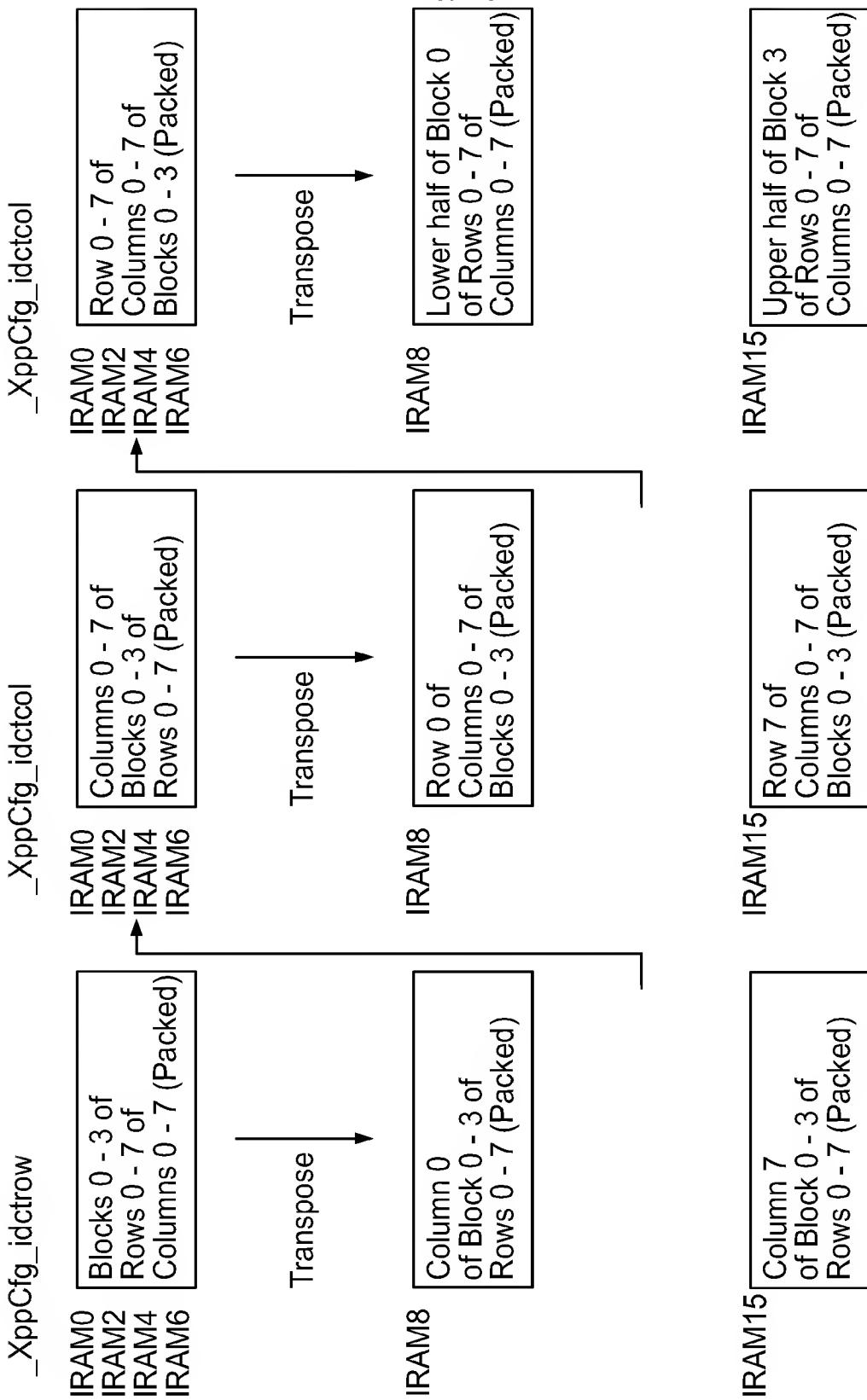


FIG. 31

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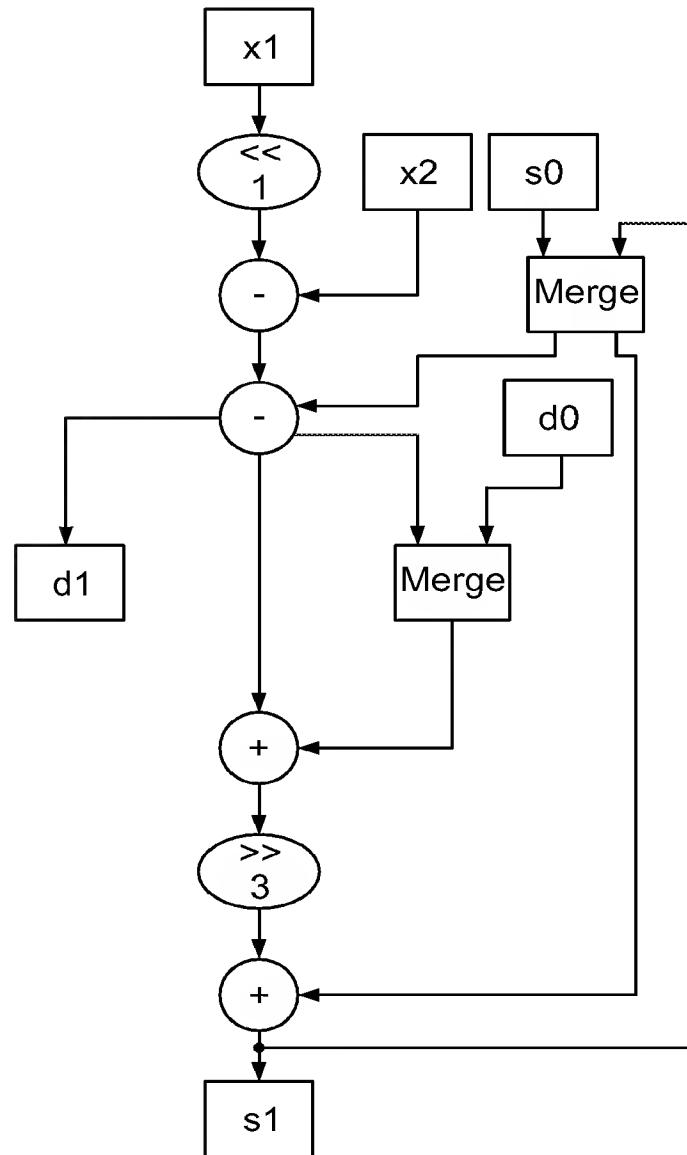


FIG. 32

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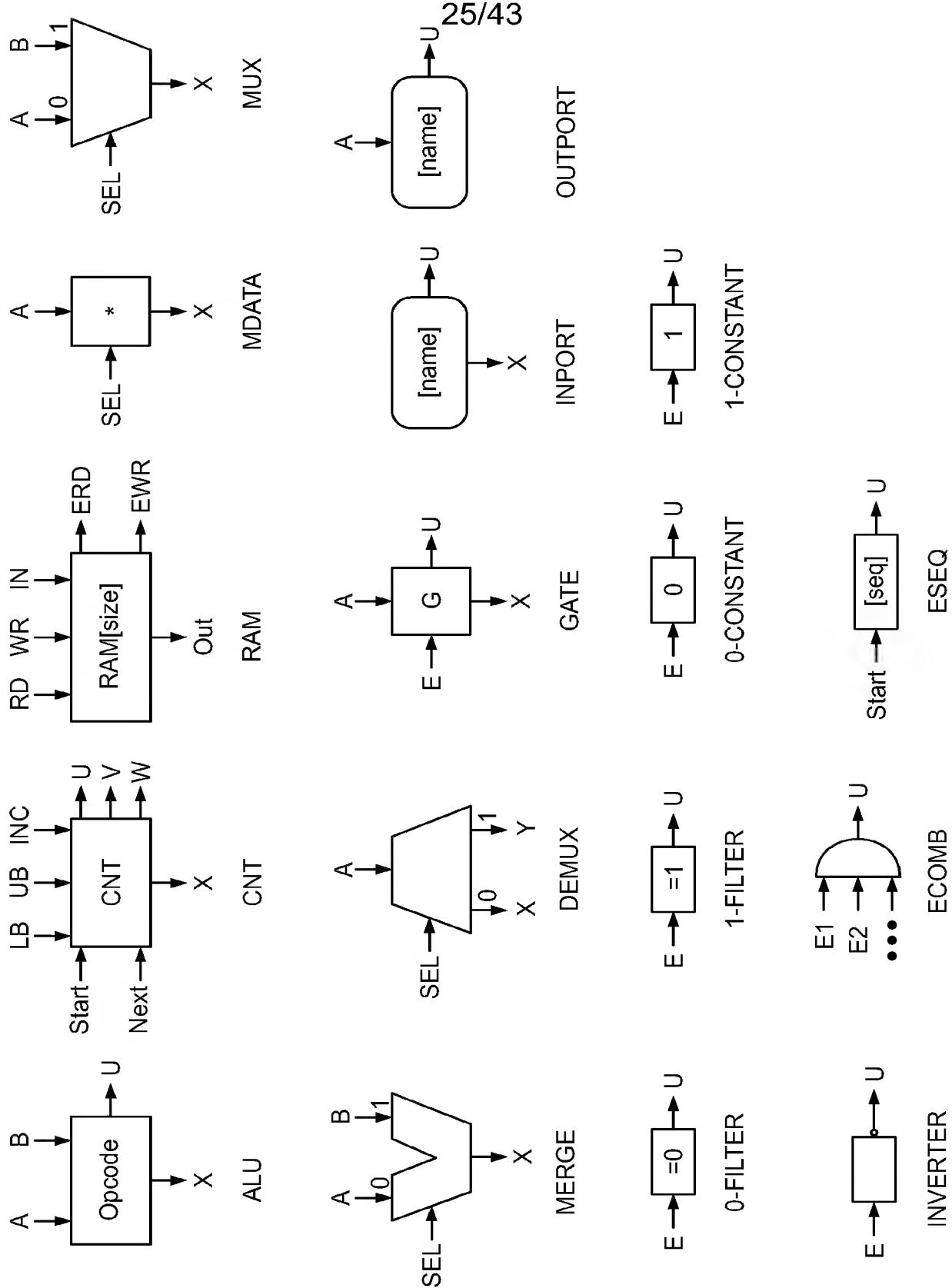


FIG. 33

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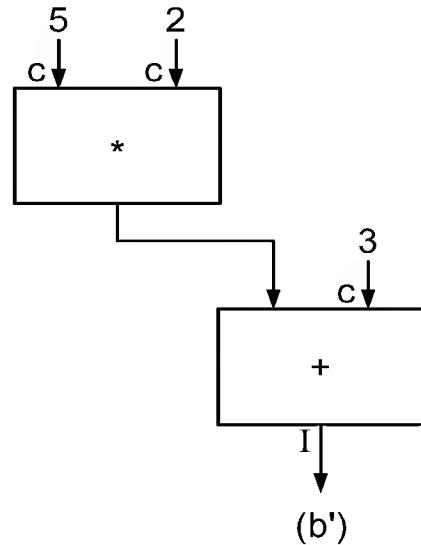


FIG. 34

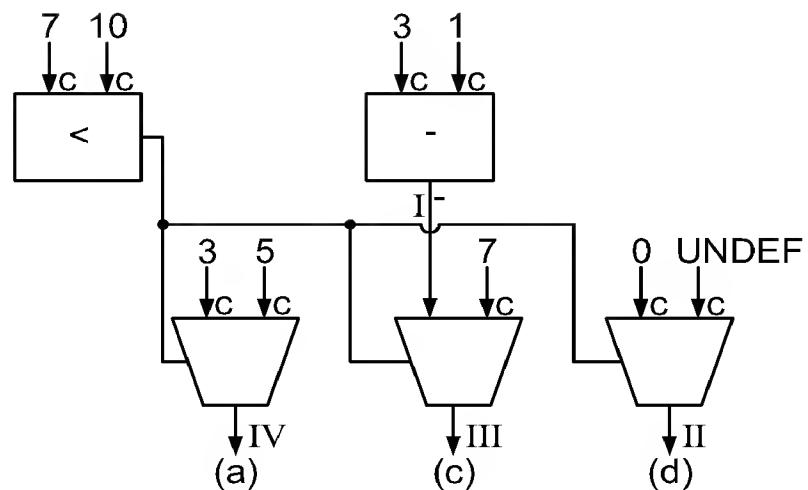


FIG. 35

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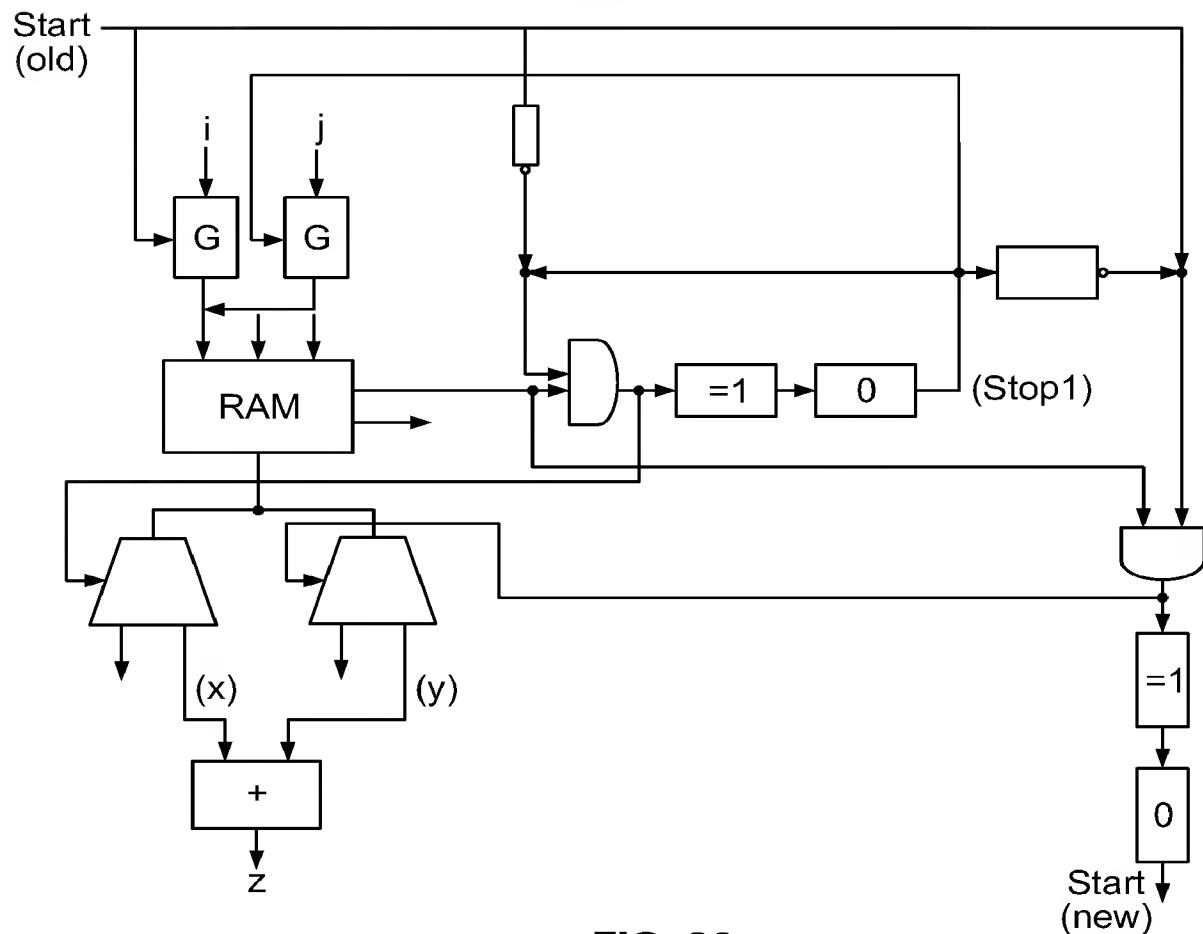


FIG. 36

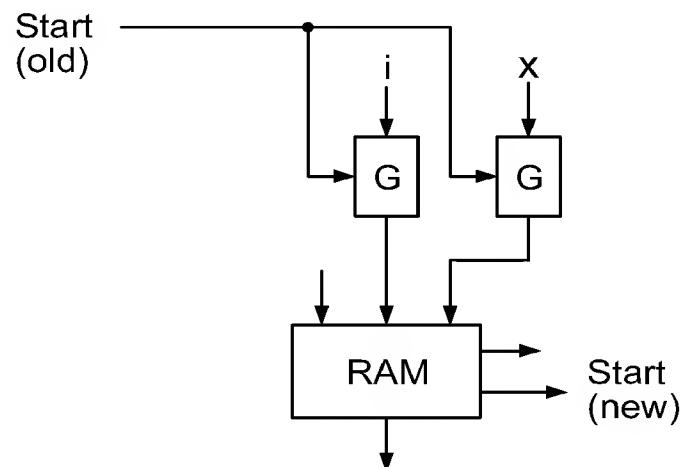


FIG. 37

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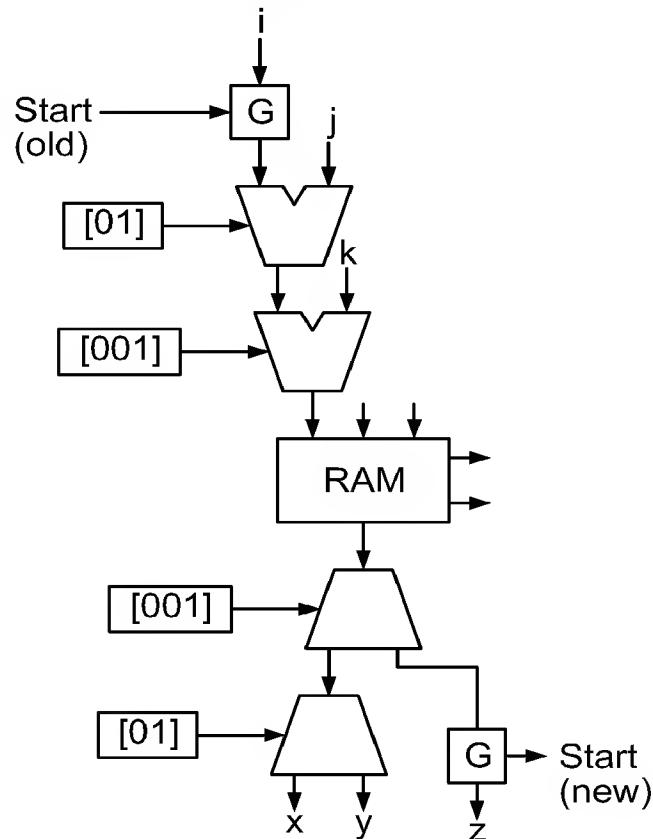


FIG. 38

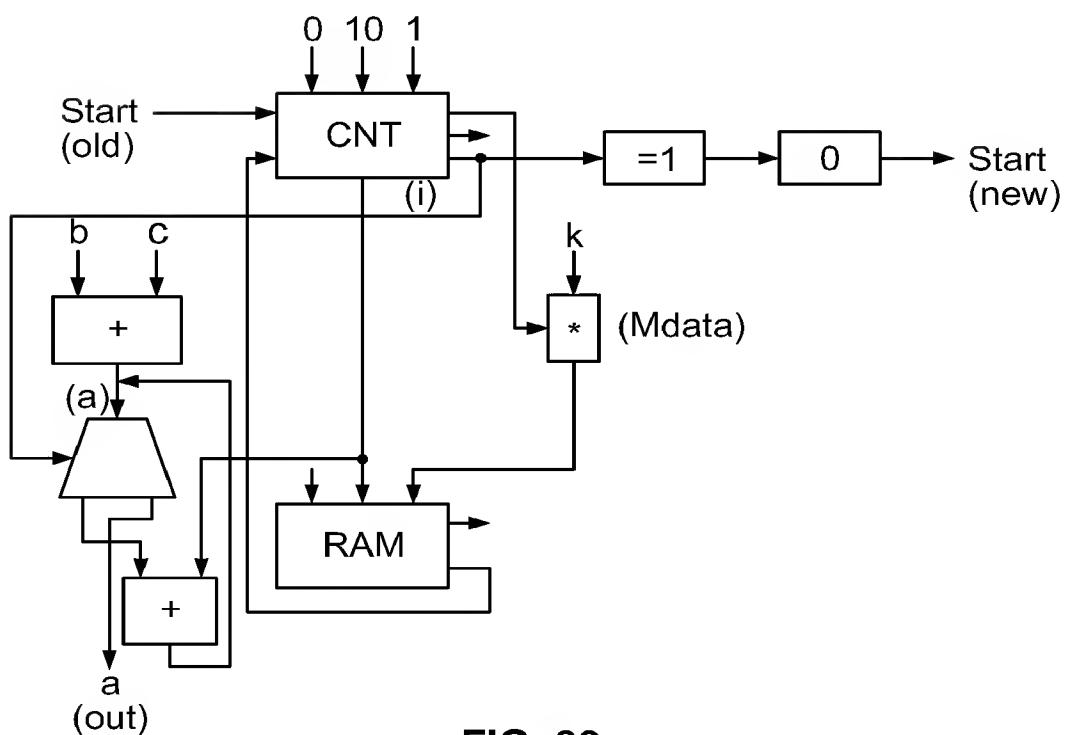


FIG. 39

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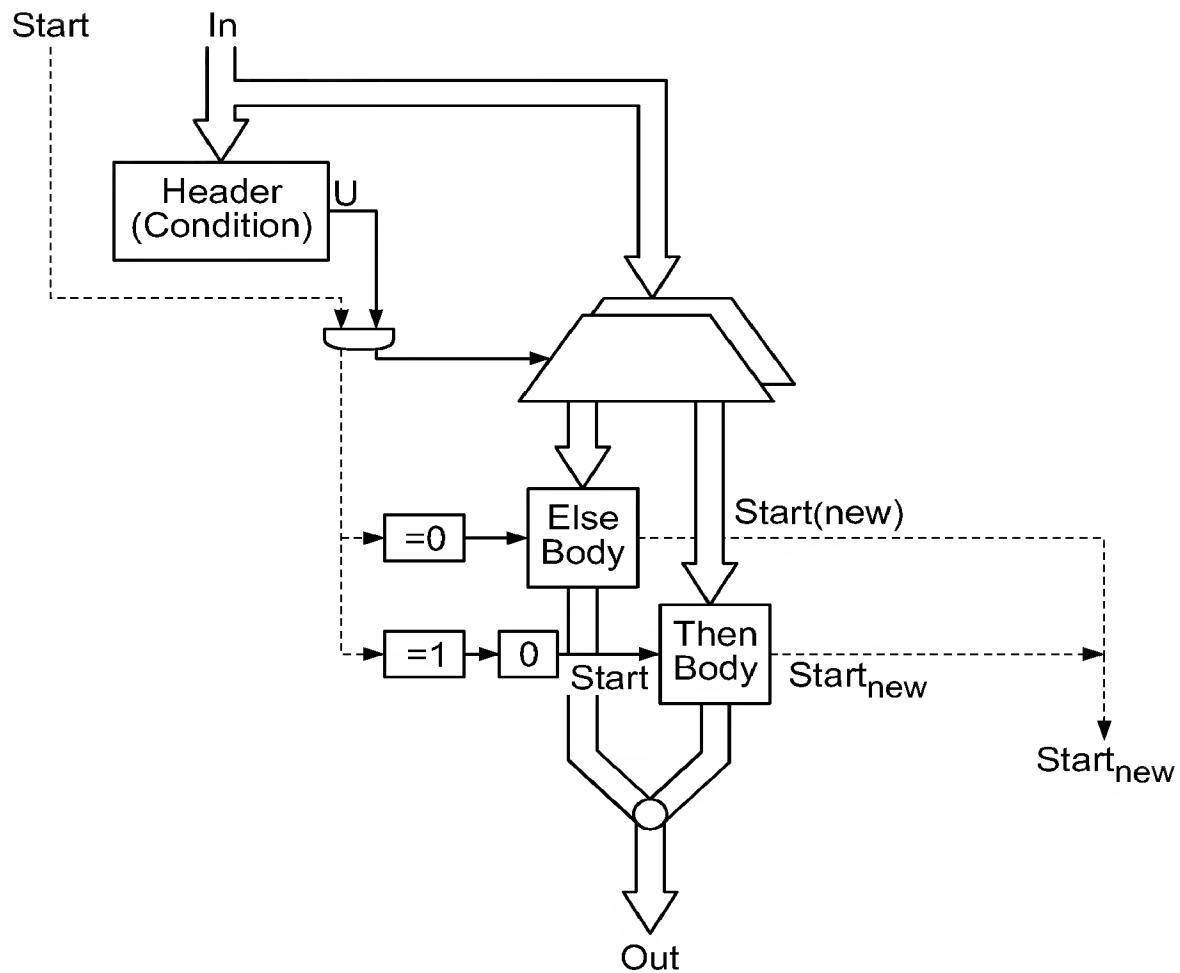


FIG. 40

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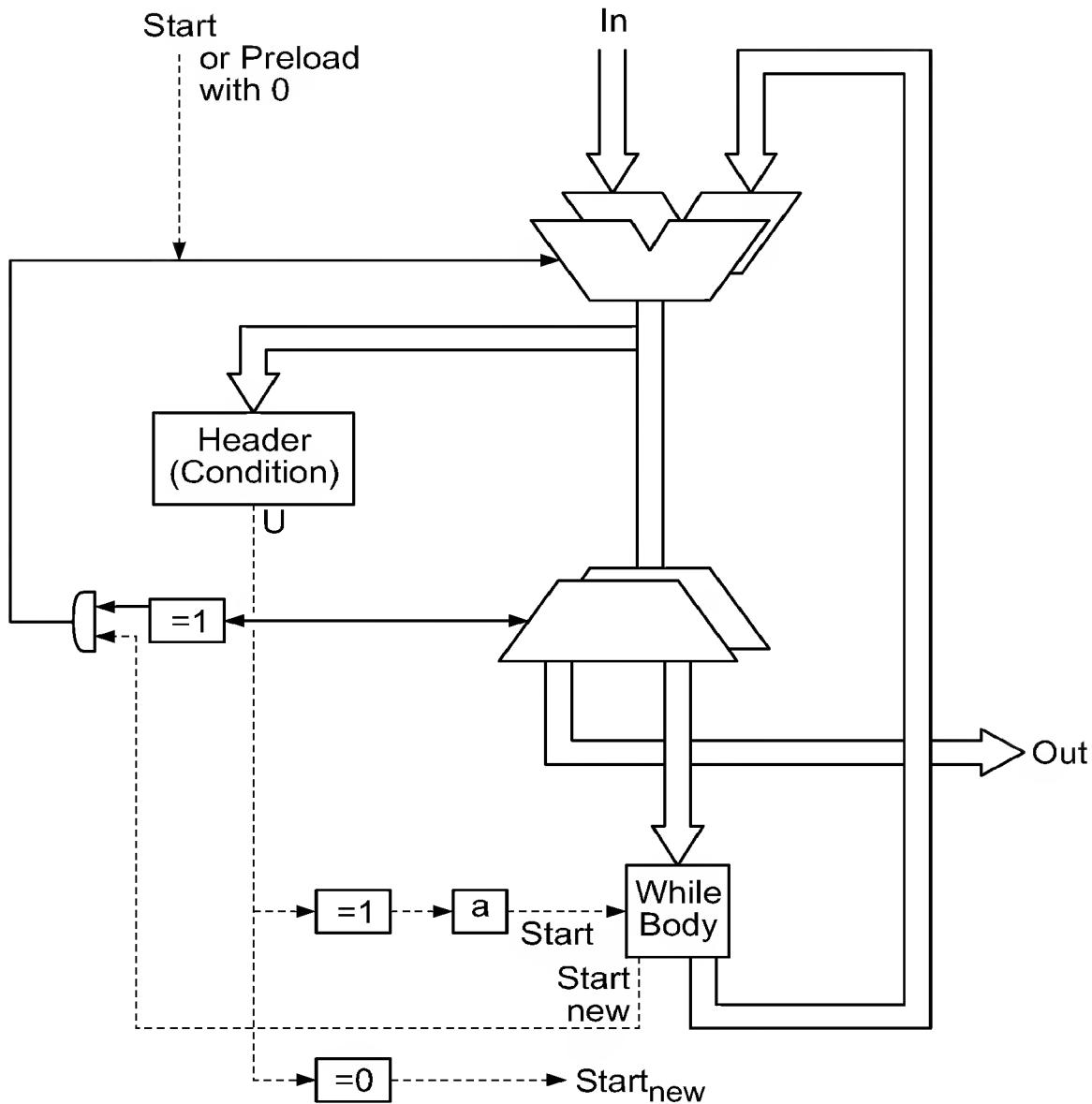


FIG. 41

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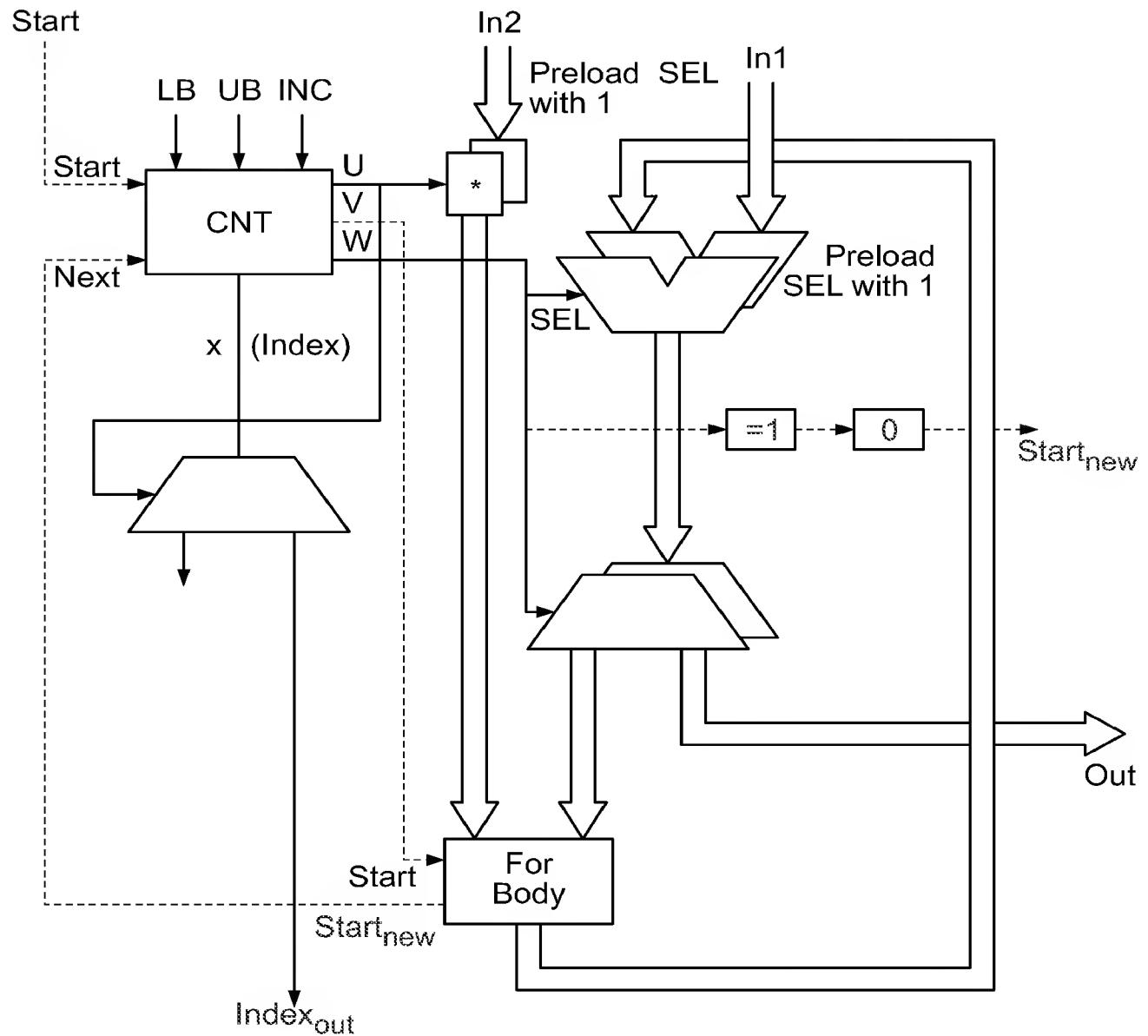


FIG. 42

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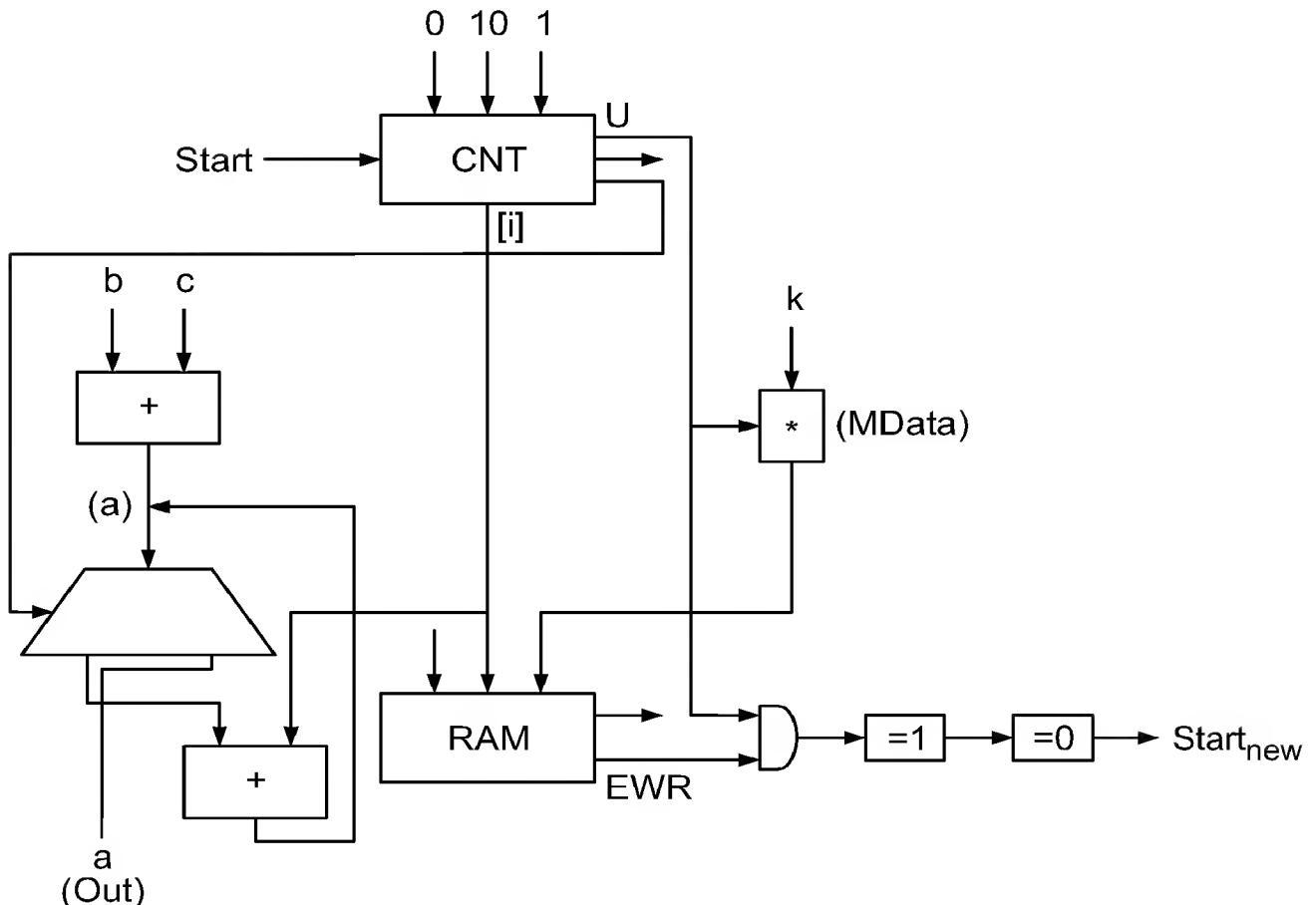


FIG. 43

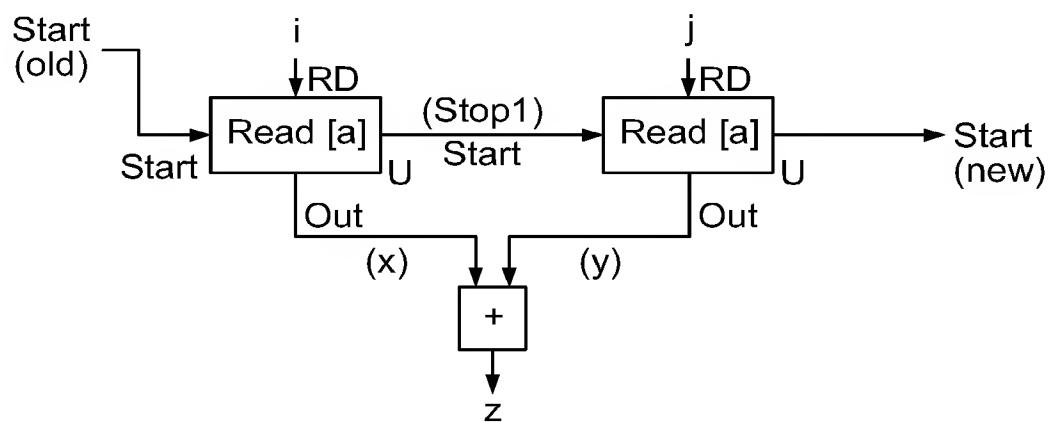


FIG. 44

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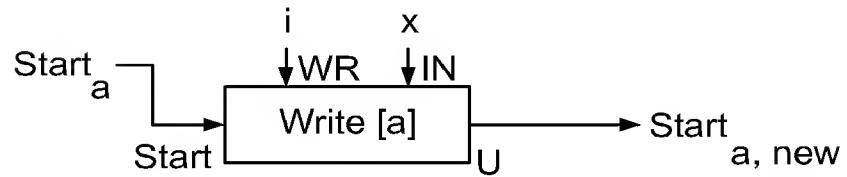


FIG. 45

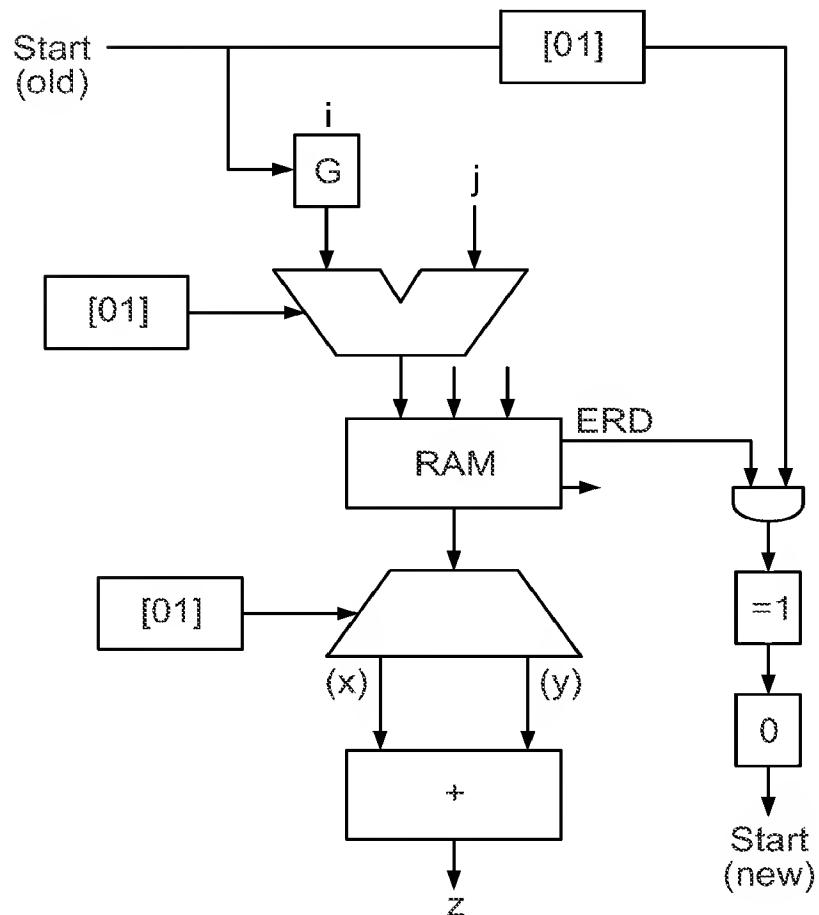


FIG. 46

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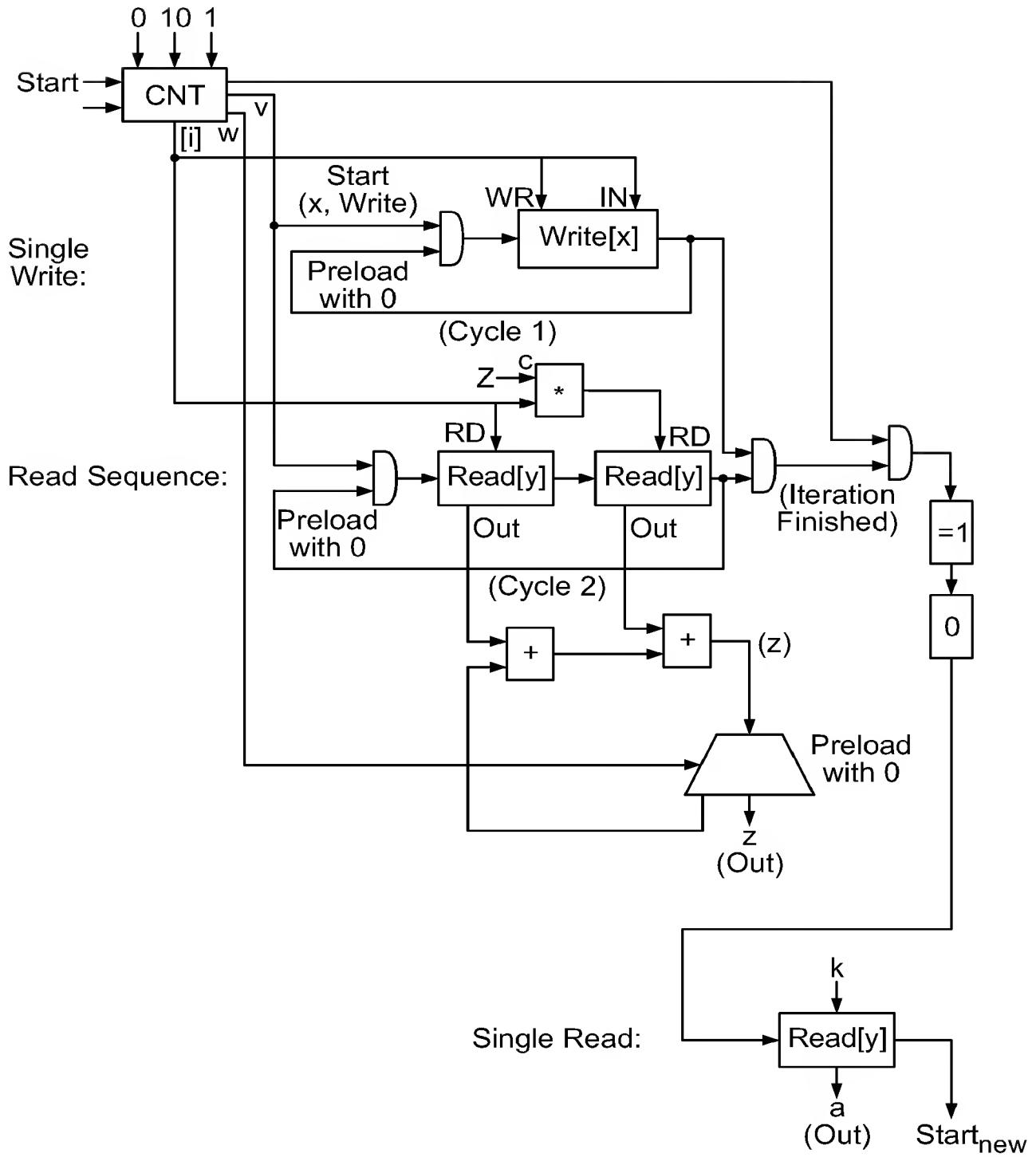


FIG. 47

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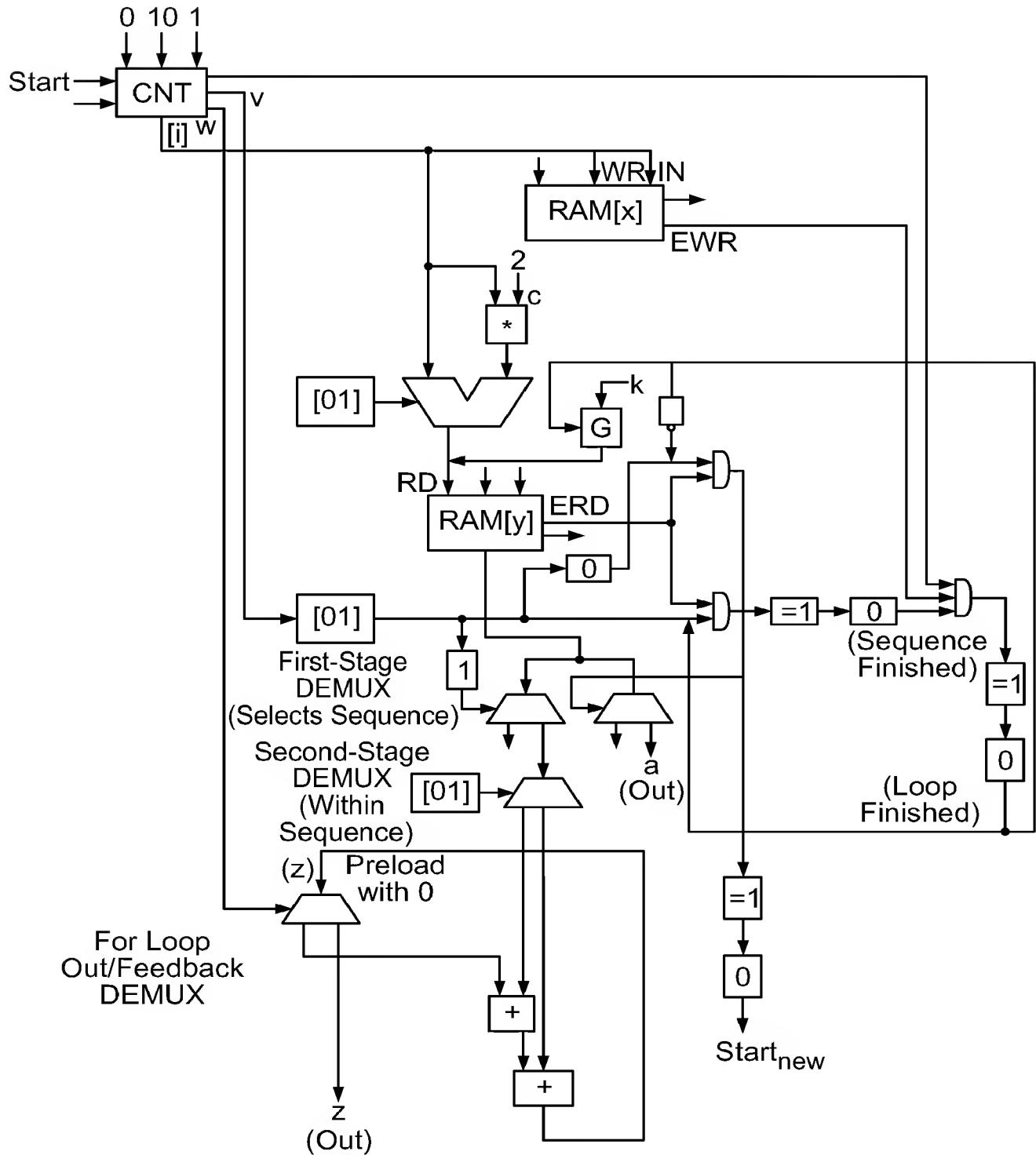


FIG. 48

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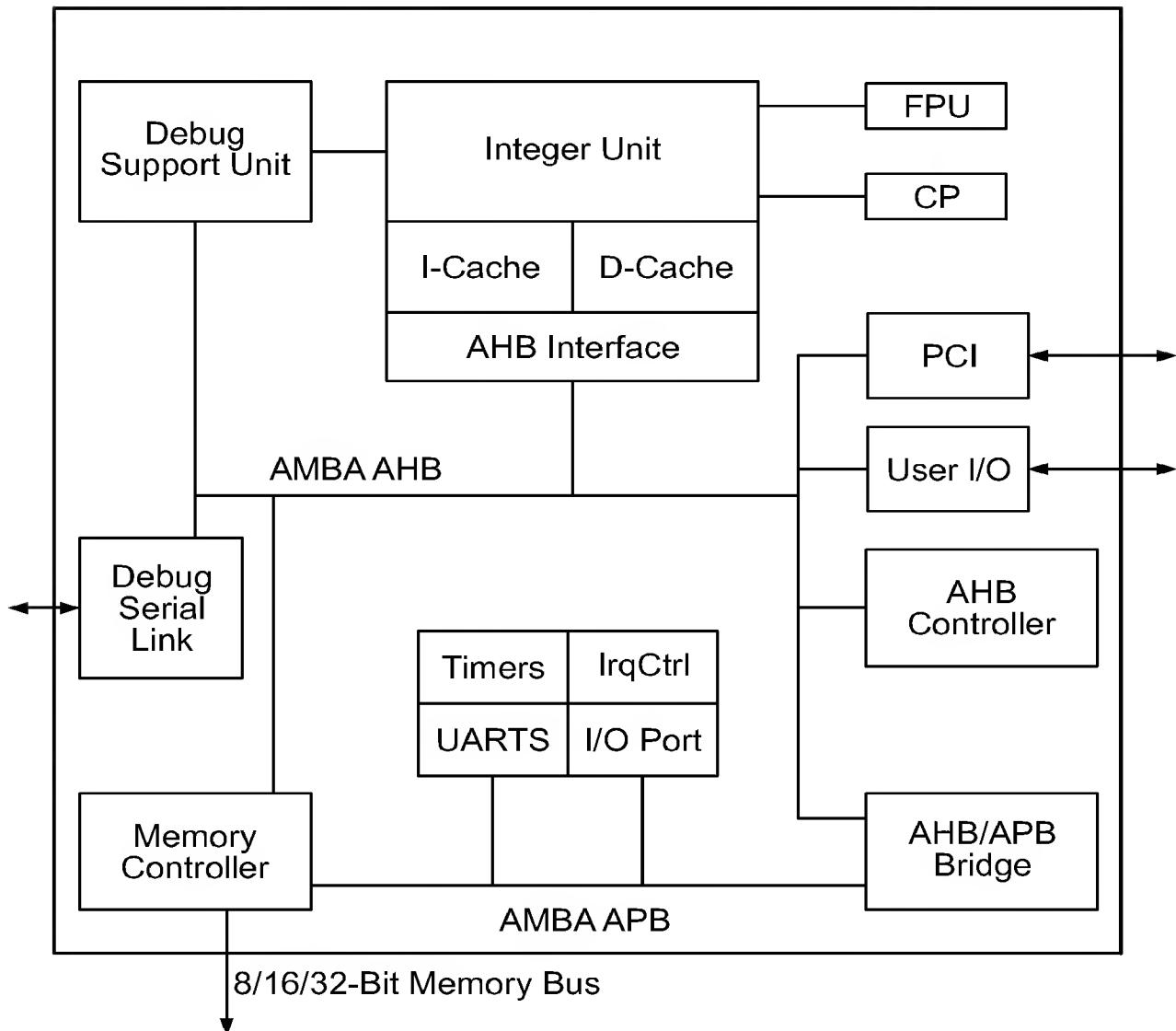


FIG. 49

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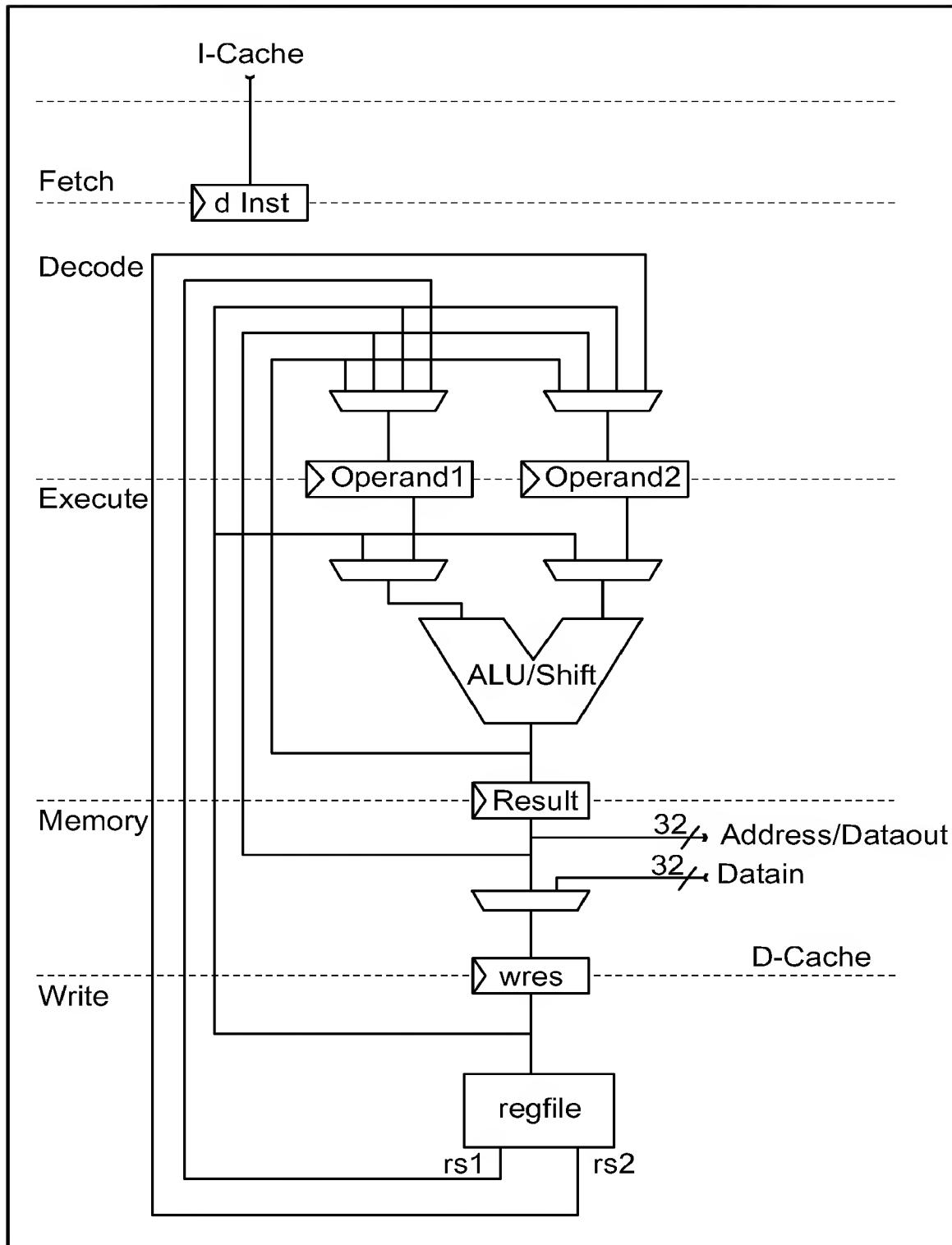


FIG. 50

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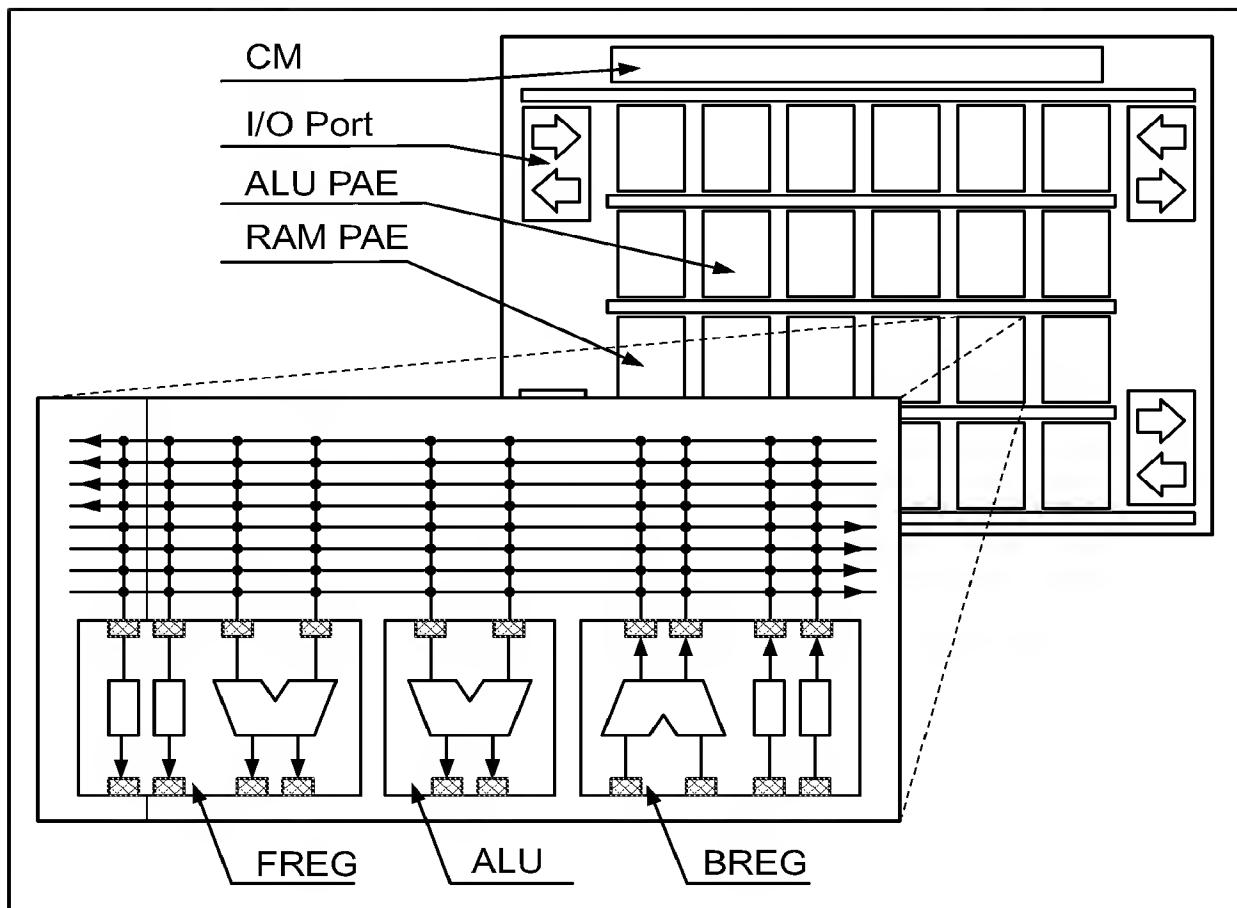


FIG. 51

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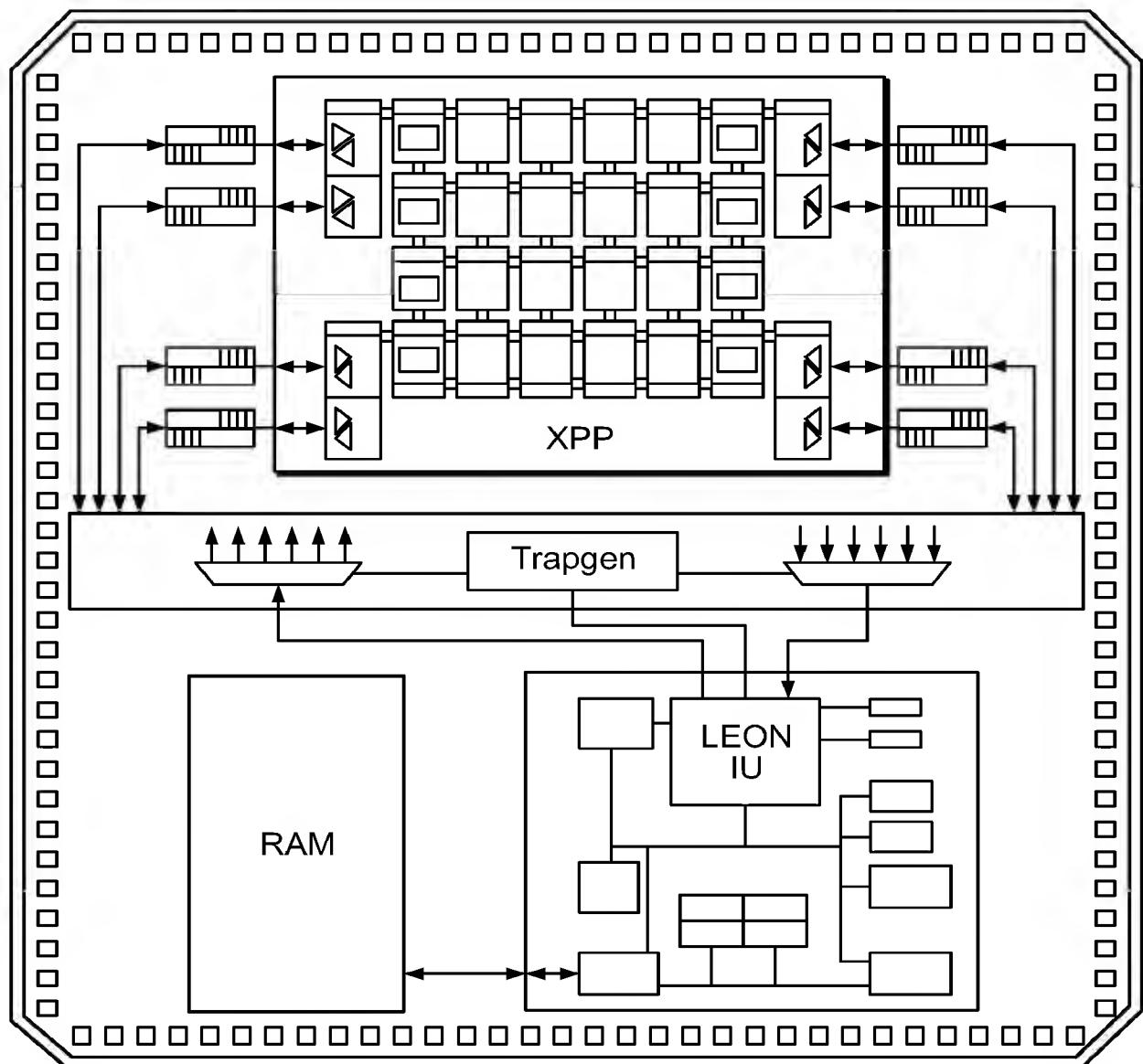


FIG. 52

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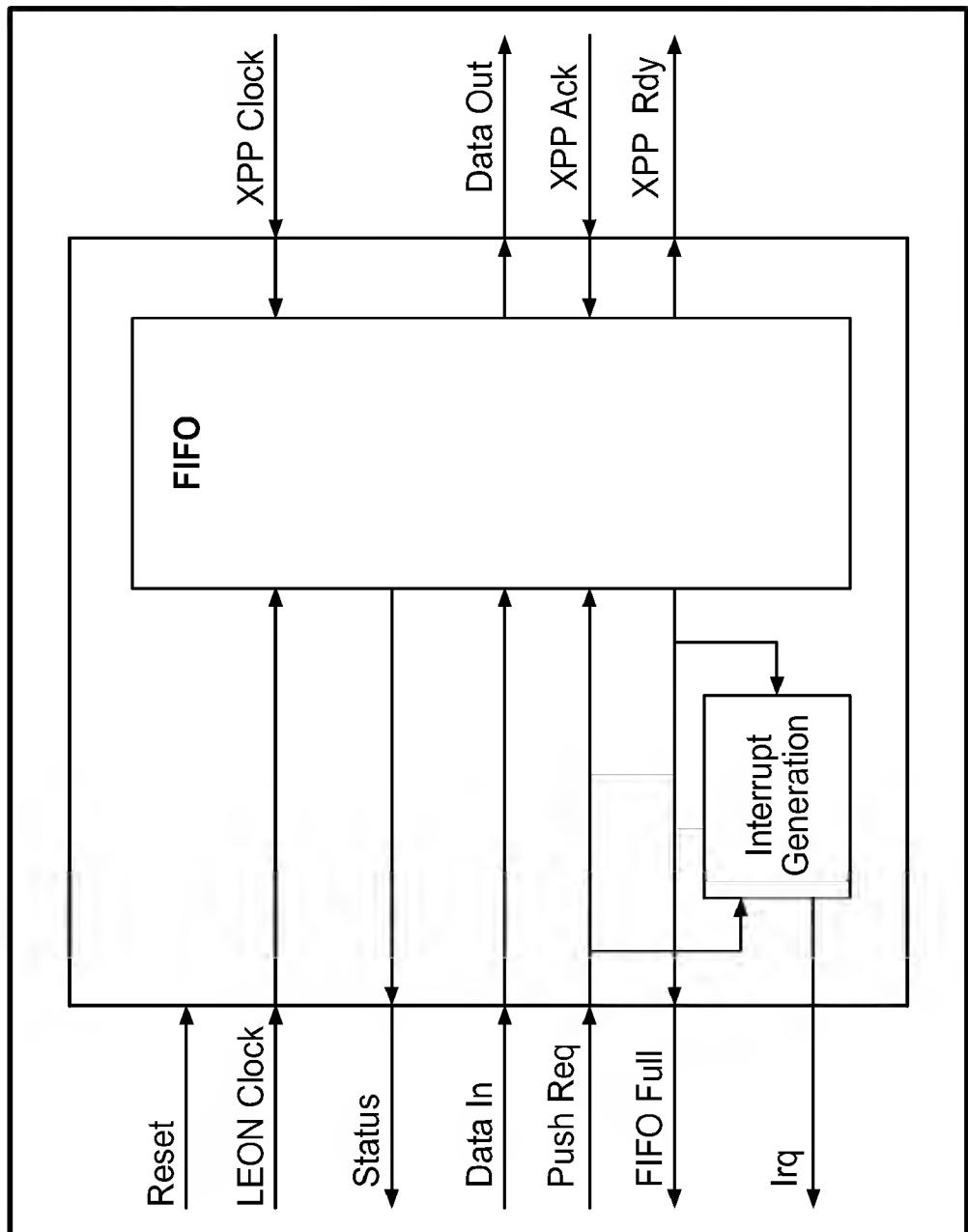


FIG. 53

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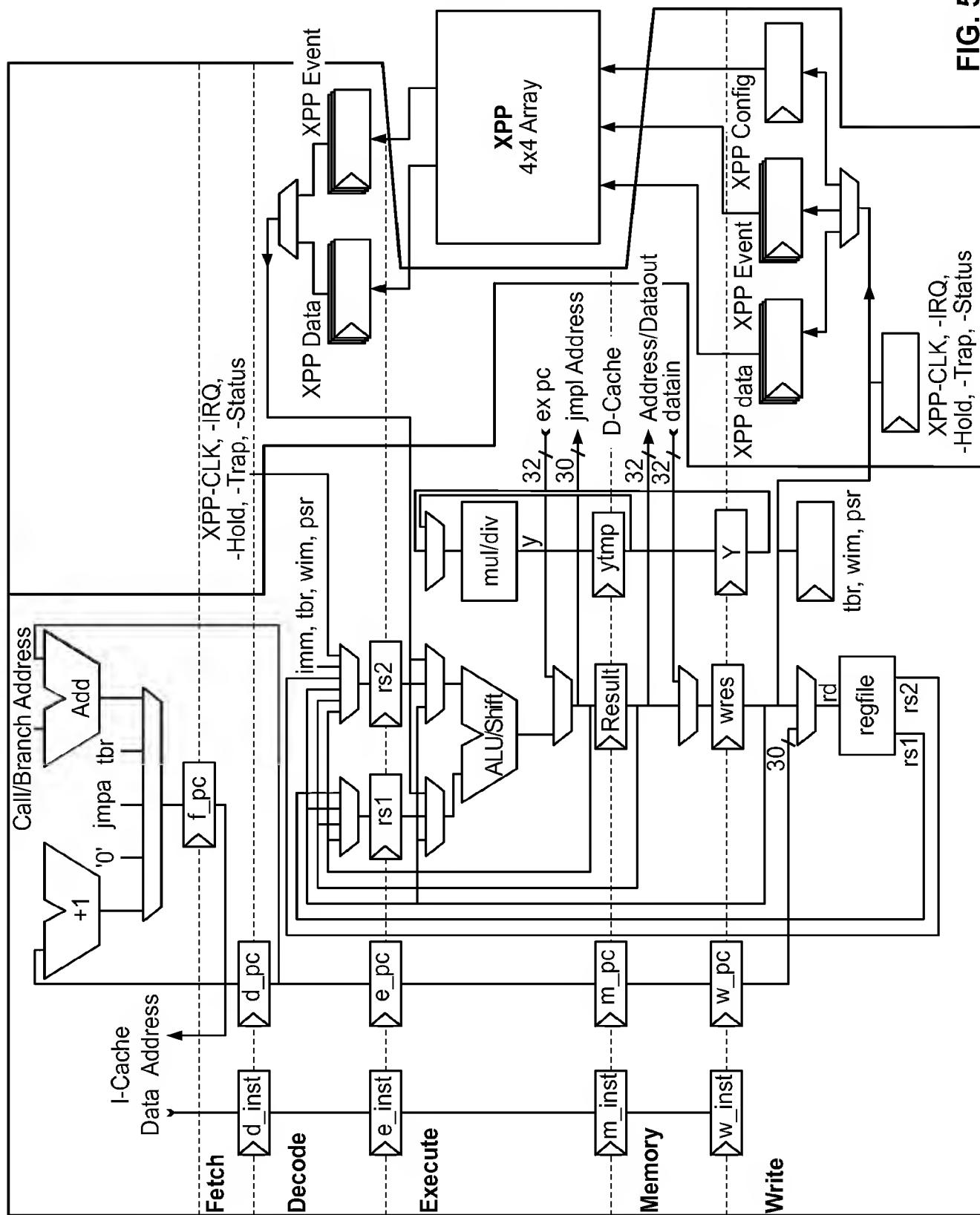


FIG. 54

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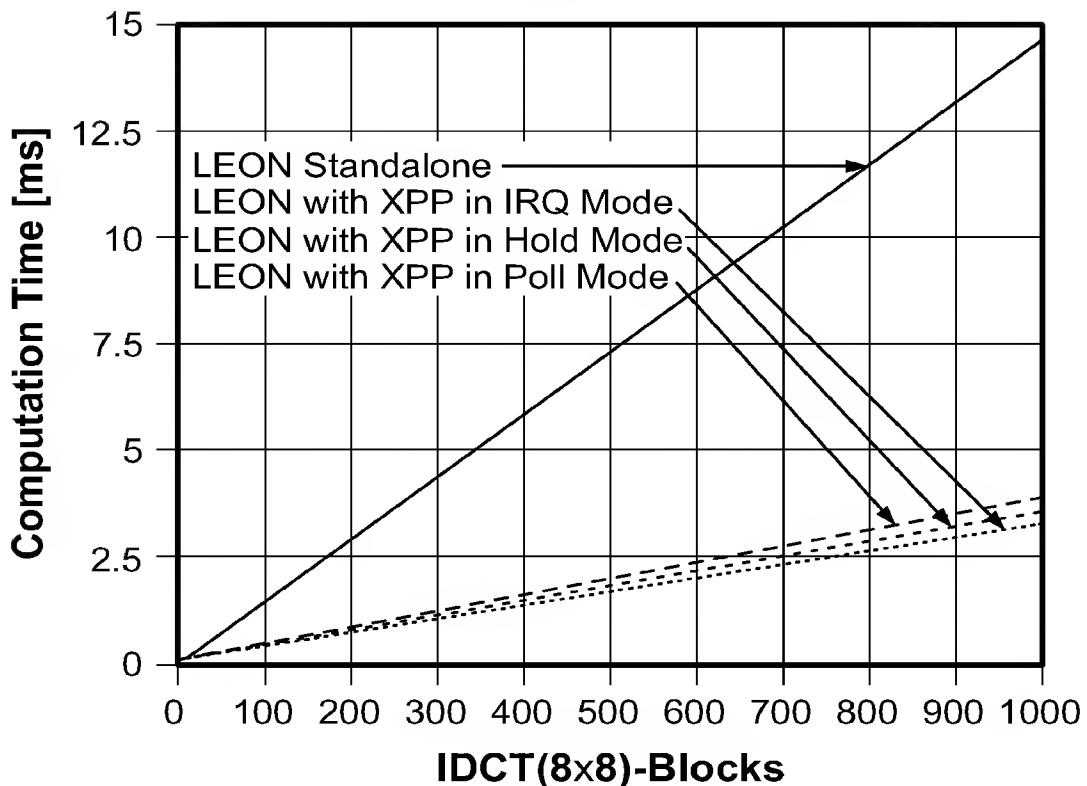


FIG. 55

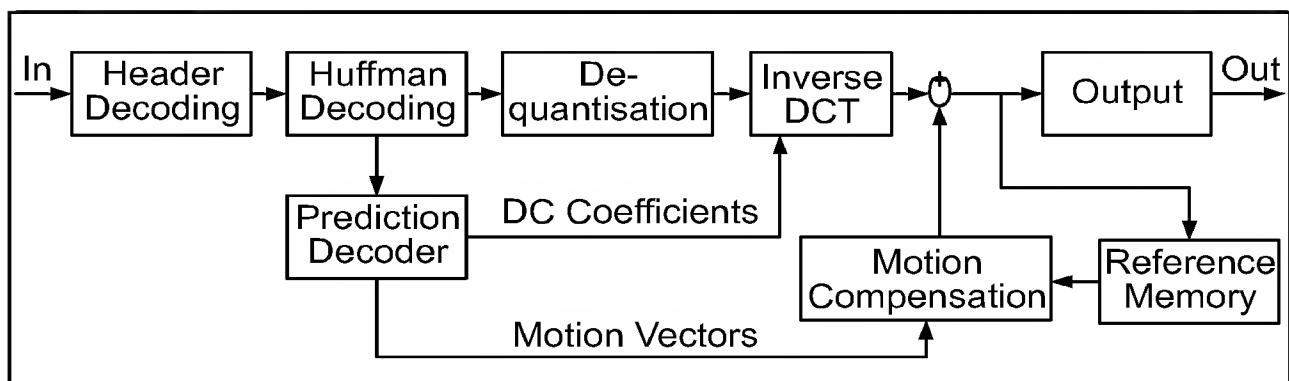


FIG. 56

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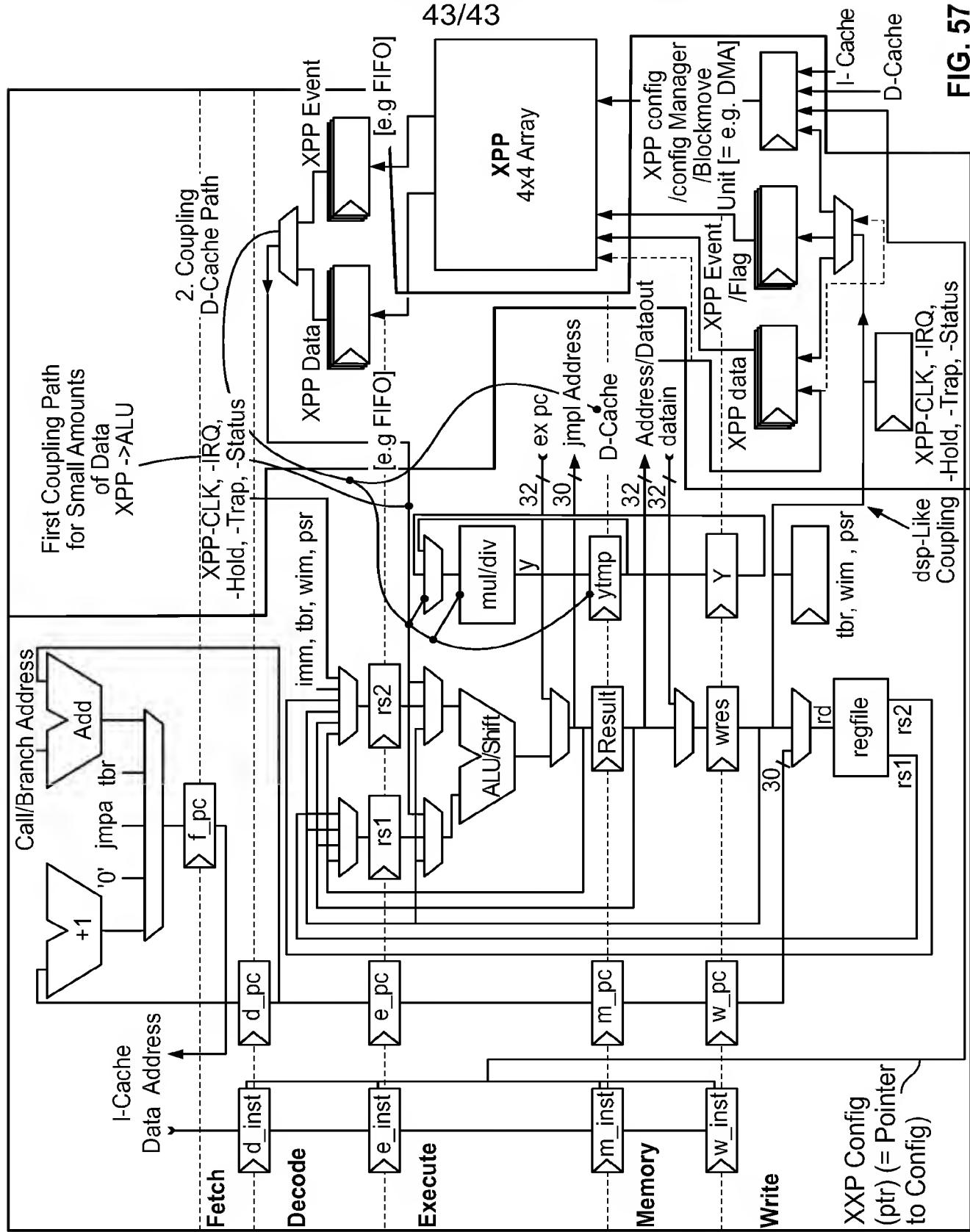


FIG. 57